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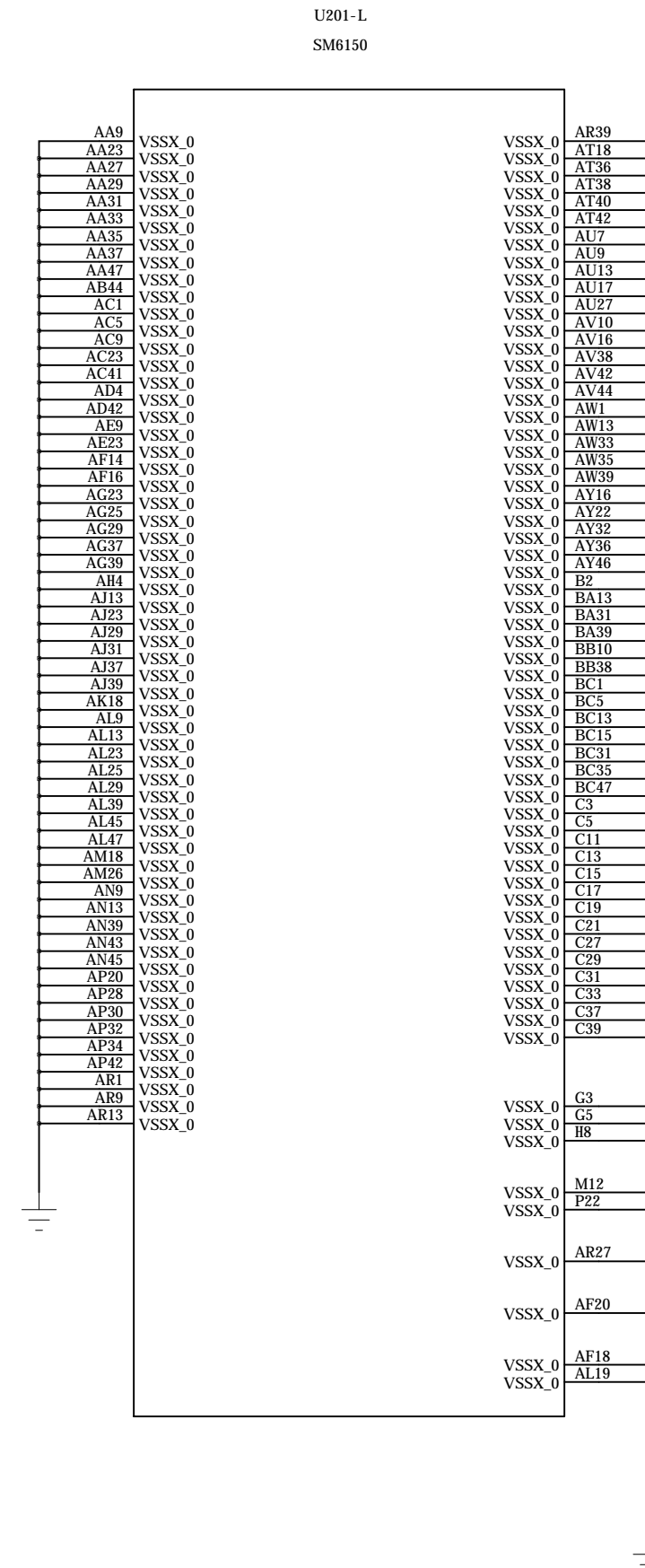
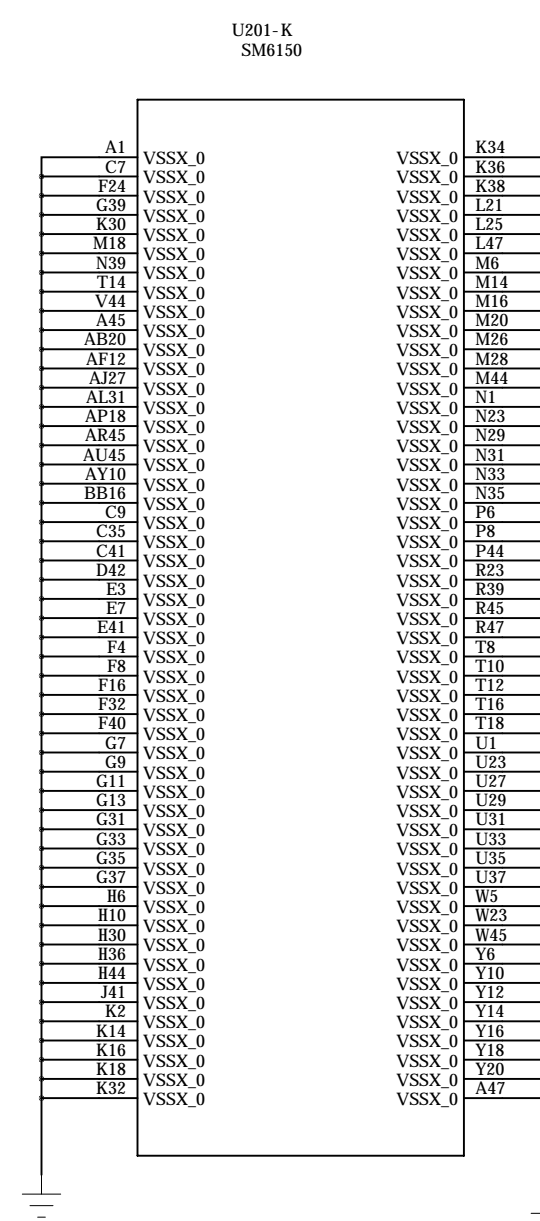
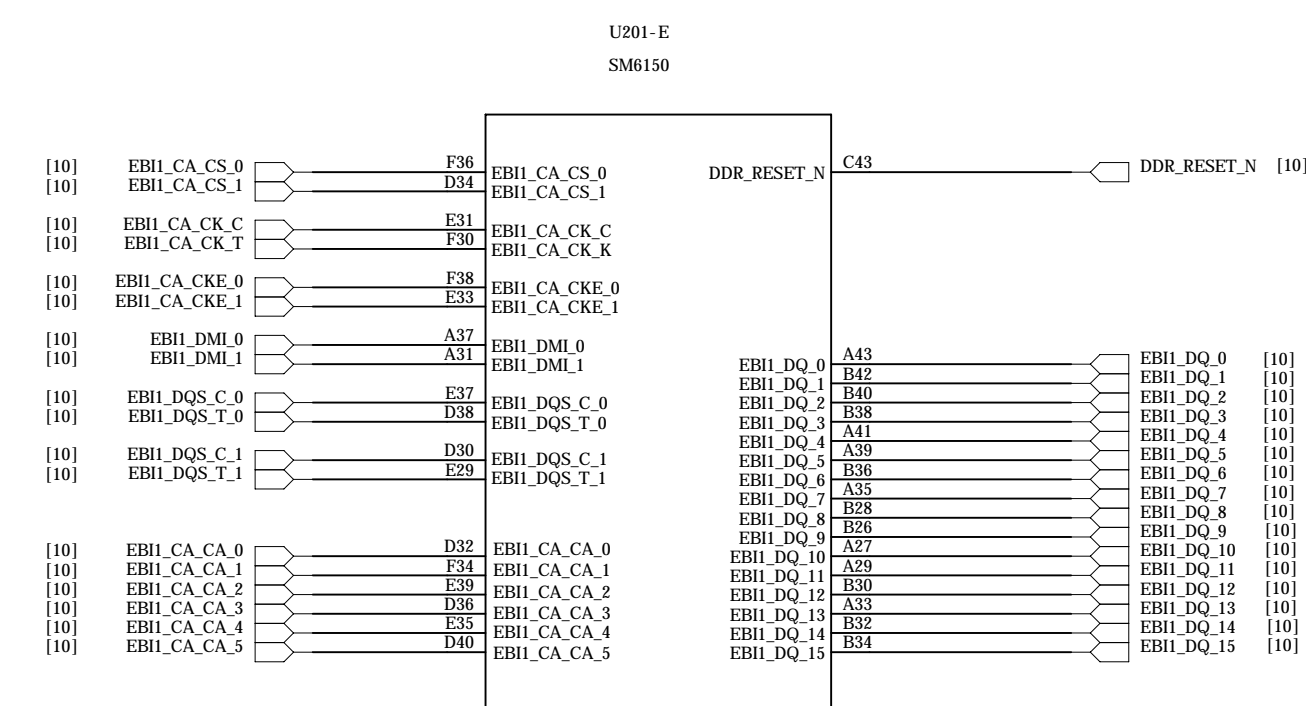
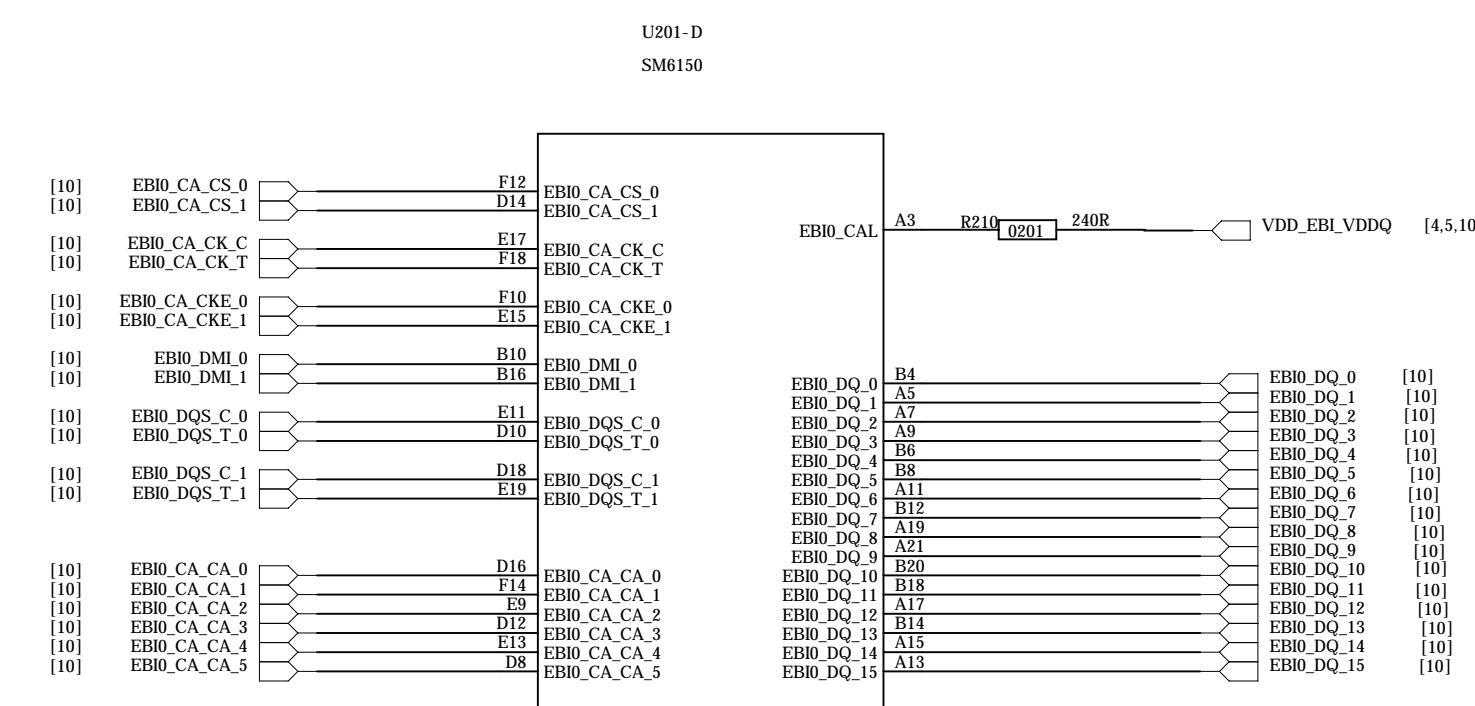
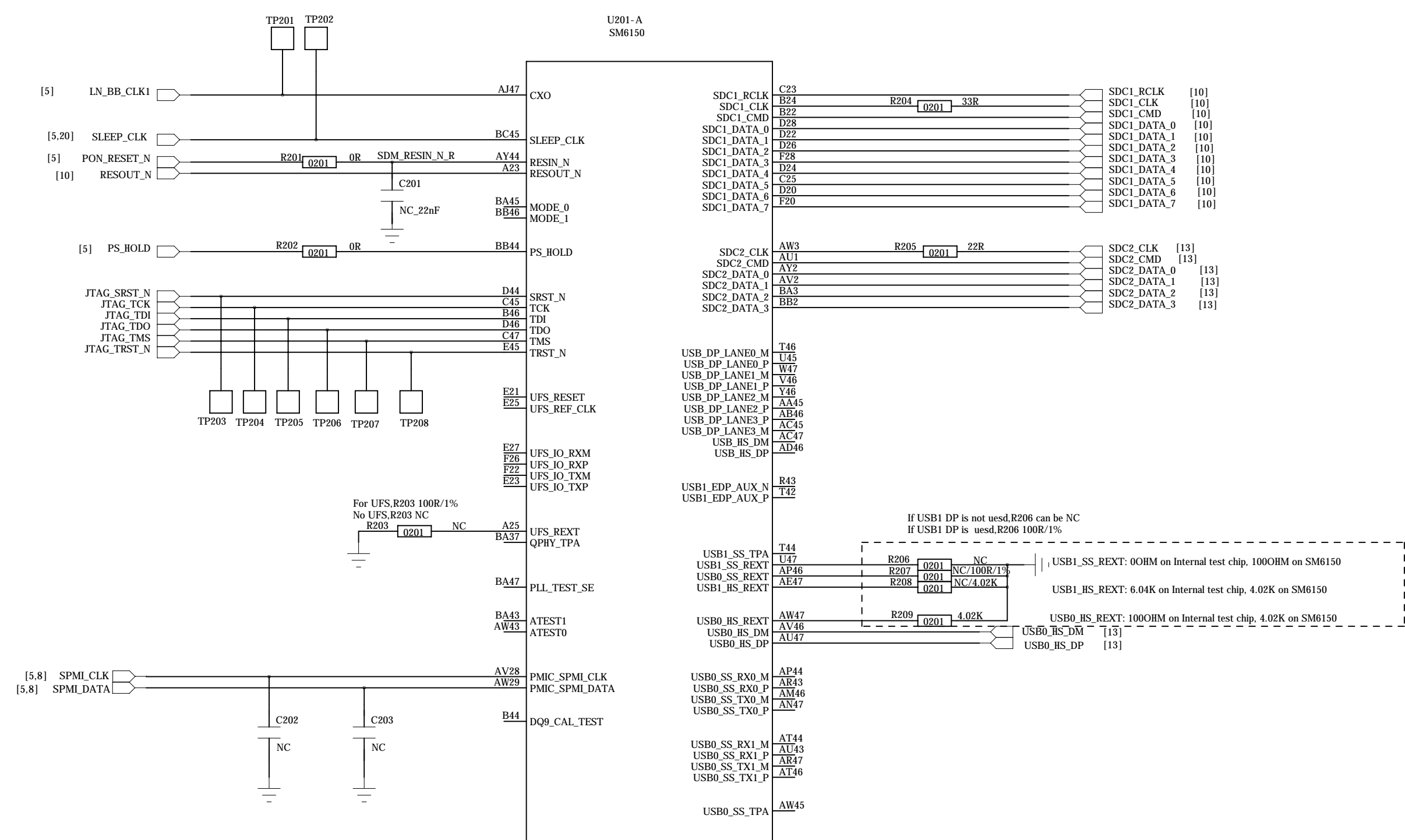
SDM636 GPIO Configuration For Starlord							
GPIO_0	FP_SPI_MISO	GPIO_50	NFC_CLKREQ	GPIO_100	NC	LPI_26	NC
GPIO_1	FP_SPI_MOSI	GPIO_51	QLINK_REQUEST	GPIO_101	FP_RESET_N	LPI_27	NC
GPIO_2	FP_SPI_SCLK	GPIO_52	QLINK_ENABLE	GPIO_102	WLAN_SW_CTRL	LPI_28	NC
GPIO_3	I2C_TP_CS_N	GPIO_53	NC	GPIO_103	LCD_ID	LPI_29	NC
GPIO_4	TS_I2C_SDA	GPIO_54	NC	GPIO_104	USB_PHY_PS	LPI_30	LPI_QCA_SB_DATA
GPIO_5	TS_I2C_SCL	GPIO_55	NC	GPIO_105	WMSS_RESET	LPI_31	LPI_QCA_SB_CLK
GPIO_6	ESB_SPI_MISO	GPIO_56	NC	GPIO_106	MSS_LTE_COXM_TXD		
GPIO_7	ESB_SPI_MOSI	GPIO_57	NC	GPIO_107	MSS_LTE_COXM_RXD		
GPIO_8	ESB_SPI_SCLK	GPIO_58	NC	GPIO_108	WSA_SPKR_SD_N_1		
GPIO_9	ESB_SPI_CS_N	GPIO_59	NC	GPIO_109	NC		
GPIO_10	BT_UART_CTS_N	GPIO_60	NC	GPIO_110	WSA_DATA		
GPIO_11	BT_UART_RFR_N	GPIO_61	RFFE1_DATA	GPIO_111	WSA_CLK		
GPIO_12	BT_UART_TX_N	GPIO_62	RFFE1_CLK	GPIO_112	RCM_MARKER2		
GPIO_13	BT_UART_RX_N	GPIO_63	RFFE2_DATA	GPIO_113	NC		
GPIO_14	NFC_I2C_SDA	GPIO_64	RFFE2_CLK	GPIO_114	NC		
GPIO_15	NFC_I2C_SCL	GPIO_65	RFFE3_DATA	GPIO_115	NC		
GPIO_16	DEBUG_UART_TX	GPIO_66	RFFE3_CLK	GPIO_116	NC		
GPIO_17	DEBUG_UART_RX	GPIO_67	RFFE4_DATA	GPIO_117	NC		
GPIO_18	SHARED_I2C_SDA	GPIO_68	RFFE4_CLK	GPIO_118	NC		
GPIO_19	SHARED_I2C_SCL	GPIO_69	RFFE5_DATA	GPIO_119	NC		
GPIO_20	NC	GPIO_70	RFFE5_CLK	GPIO_120	NC		
GPIO_21	NC	GPIO_71	RFFE6_CLK	GPIO_121	NC		
GPIO_22	NC	GPIO_72	RFFE6_DATA	GPIO_122	NC		
GPIO_23	NC	GPIO_73	UIM2_DATA				
GPIO_24	NC	GPIO_74	UIM2_CLK	LPI_0	SSC_I2C_1_SDA		
GPIO_25	NC	GPIO_75	UIM2_RESET	LPI_1	SSC_I2C_1_SCL		
GPIO_26	NC	GPIO_76	UIM2_PRESENT	LPI_2	SSC_SPI1_MISO		
GPIO_27	NC	GPIO_77	UIM1_DATA	LPI_3	SSC_SPI1_MOSI		
GPIO_28	CAM_MCLK0	GPIO_78	UIM1_CLK	LPI_4	SSC_SPI1_SCLK		
GPIO_29	CAM_MCLK1	GPIO_79	UIM1_RESET	LPI_5	SSC_SPI1_CS0_N		
GPIO_30	CAM_MCLK2	GPIO_80	UIM1_PRESENT	LPI_6	NC		
GPIO_31	CAM_MCLK3	GPIO_81	ACCEL_INT	LPI_7	NC		
GPIO_32	CCI_I2C_SDA0	GPIO_82	GYRO_INT	LPI_8	TS_SPI_MISO		
GPIO_33	CCI_I2C_SCL0	GPIO_83	MAG_DRDY_INT	LPI_9	TS_SPI_MOSI		
GPIO_34	CCI_I2C_SDA1	GPIO_84	NFC_ENABLE	LPI_10	TS_SPI_SCLK		
GPIO_35	CCI_I2C_SCL1	GPIO_85	NFC_DWL_REQ	LPI_11	TS_SPI_CS_N		
GPIO_36	NC	GPIO_86	NFC_IRQ	LPI_12	LPI_UART1_TX		
GPIO_37	CAM2_RST_N	GPIO_87	NC	LPI_13	LPI_UART1_RX		
GPIO_38	NC	GPIO_88	TS_TRESET_M	LPI_14	LPI_UART2_TX		
GPIO_39	FL_STROBE_TRIG	GPIO_89	TS_INT_N	LPI_15	LPI_UART2_RX		
GPIO_40	NC	GPIO_90	MDP_VSYNC_P	LPI_16	NC		
GPIO_41	NC	GPIO_91	LCD0_RESET_N	LPI_17	NC		
GPIO_42	NC	GPIO_92	ALSPQ_INT_N	LPI_18	SWR_TX_CLK		
GPIO_43	FROCED_USB_BOOT_POLARITY	GPIO_93	FP_INT_N1	LPI_19	SWR_TX_DATA1		
GPIO_44	NC	GPIO_94	FP_INT_N2	LPI_20	SWR_TX_DATA2		
GPIO_45	CAM1_RST_N	GPIO_95	NC	LPI_21	SWR_RX_CLK		
GPIO_46	DVDD1_1P2_EN	GPIO_96	NC	LPI_22	SWR_RX_DATA1		
GPIO_47	CAM0_RST_N	GPIO_97	RCM_MARKER1	LPI_23	SWR_RX_DATA2		
GPIO_48	WCSS_PWR_REQ	GPIO_98	NC	LPI_24	CODRC_RST_N		
GPIO_49	NC	GPIO_99	SD_CARD_DET_N	LPI_25	NC		

PM6150	
GPIO_01	NC
GPIO_02	NC
GPIO_03	SMB_STAT
GPIO_04	NC
GPIO_05	SLB
GPIO_06	SD_CARD_DET_N
GPIO_07	NFC_SLEEP_CLK
GPIO_08	NC
GPIO_09	FMB1
GPIO_10	FMB2
AMUX_THERM1	Option_1
AMUX_THERM2	MSM_THERM
AMUX_THERM3	SMB_THERM
AMUX_THERM4	CONN_THERM

PM6150L	
GPIO_01	AOSS_SLEEP_INDICATOR
GPIO_02	KYPD_VOLP_N
GPIO_03	DVDD_1P2_EN
GPIO_04	AVDD1_2P8_EN
GPIO_05	CAM_FLASH_THERM
GPIO_06	NC
GPIO_07	SLB
GPIO_08	NC
GPIO_09	AVDD0_2P8_EN
GPIO_10	QUIET_THERM
GPIO_11	NC
GPIO_12	NC
AMUX_THERM1	EMMC_THERM
AMUX_THERM2	PA_THERM0
AMUX_THERM3	NC

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REVISION RECORD			
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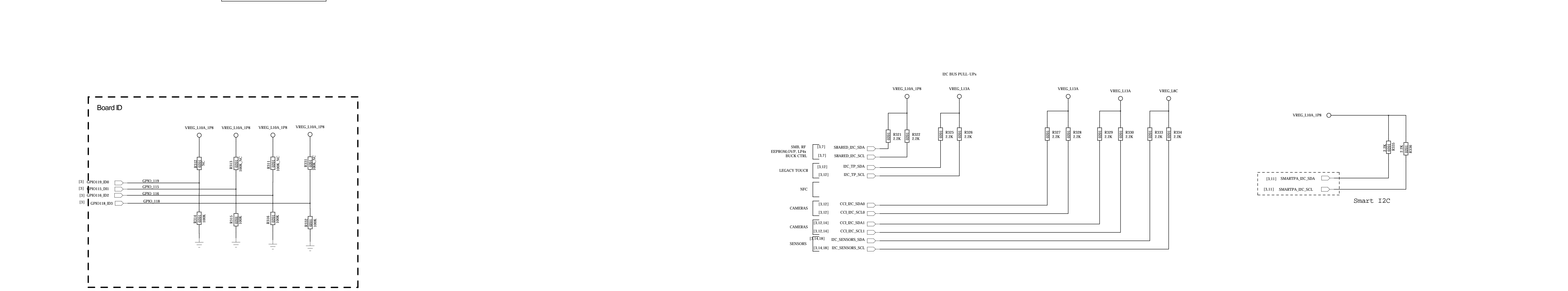
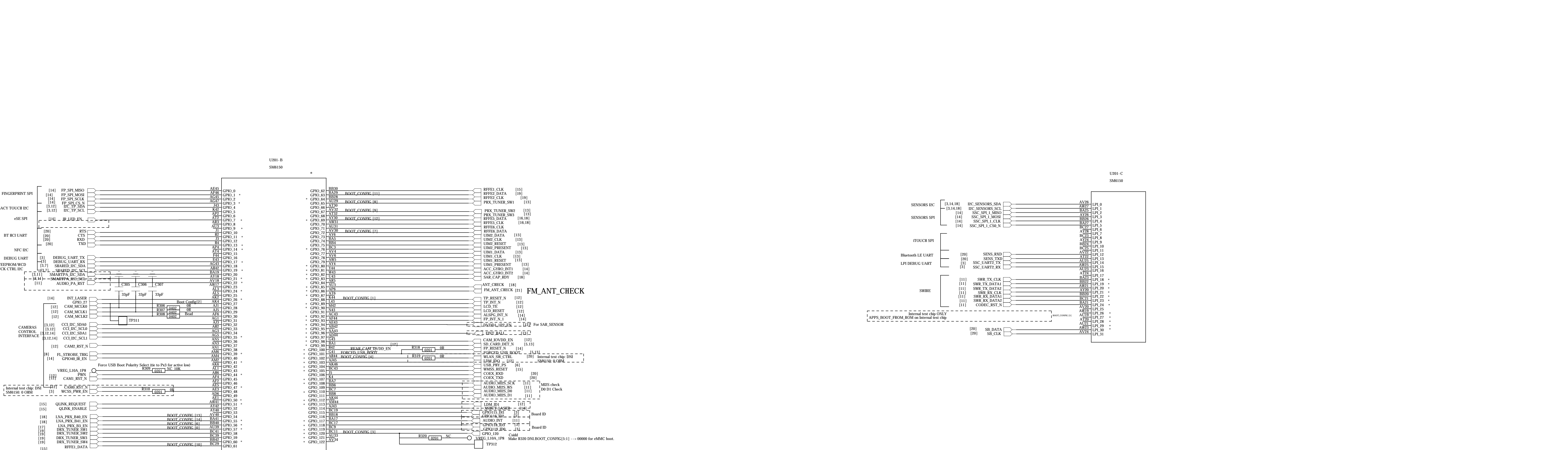
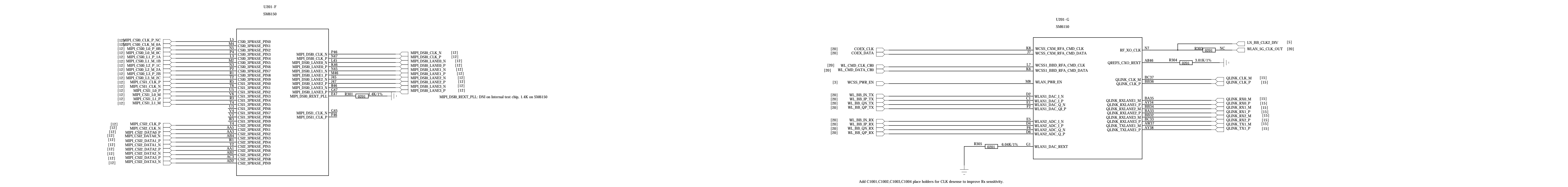
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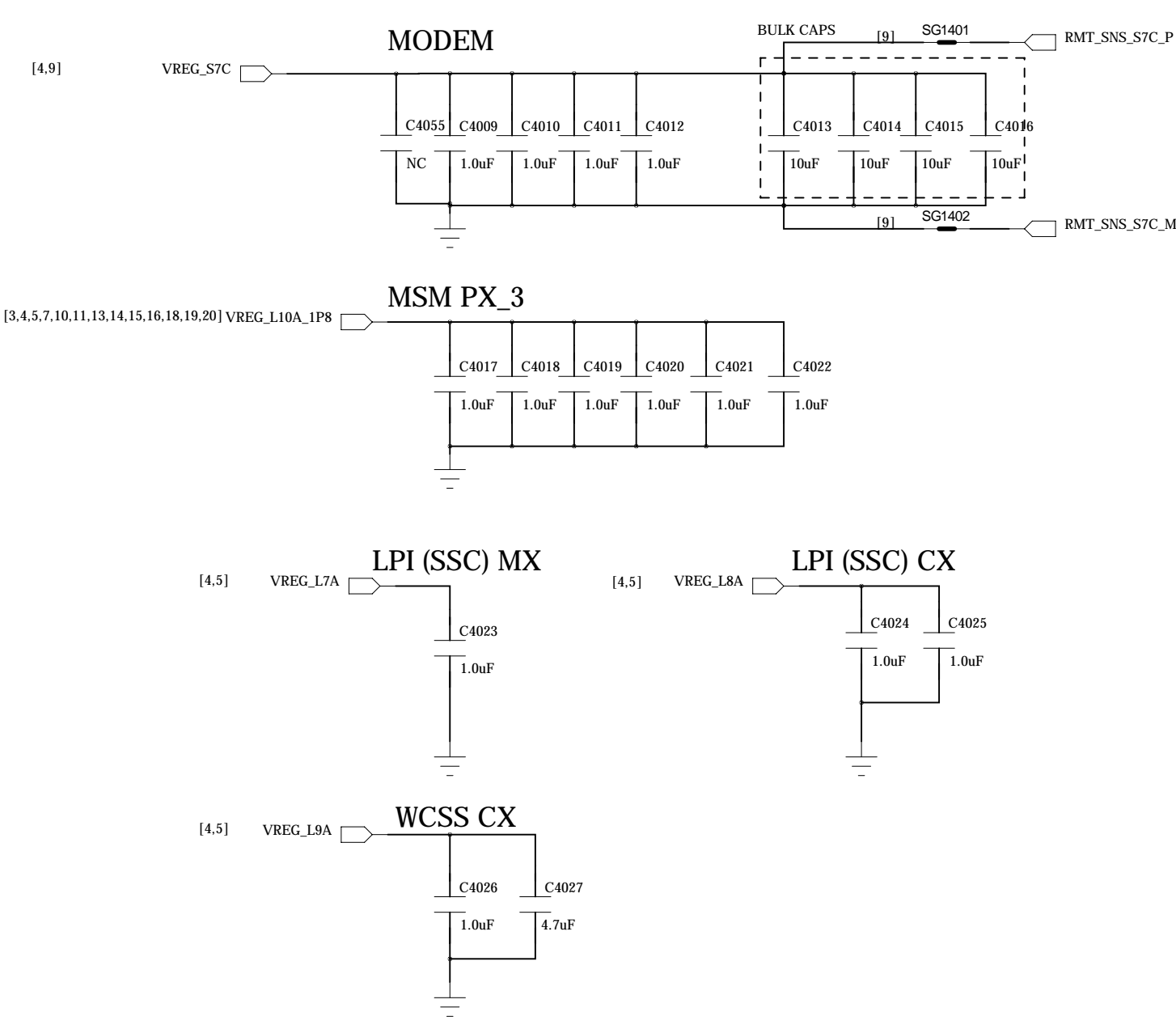
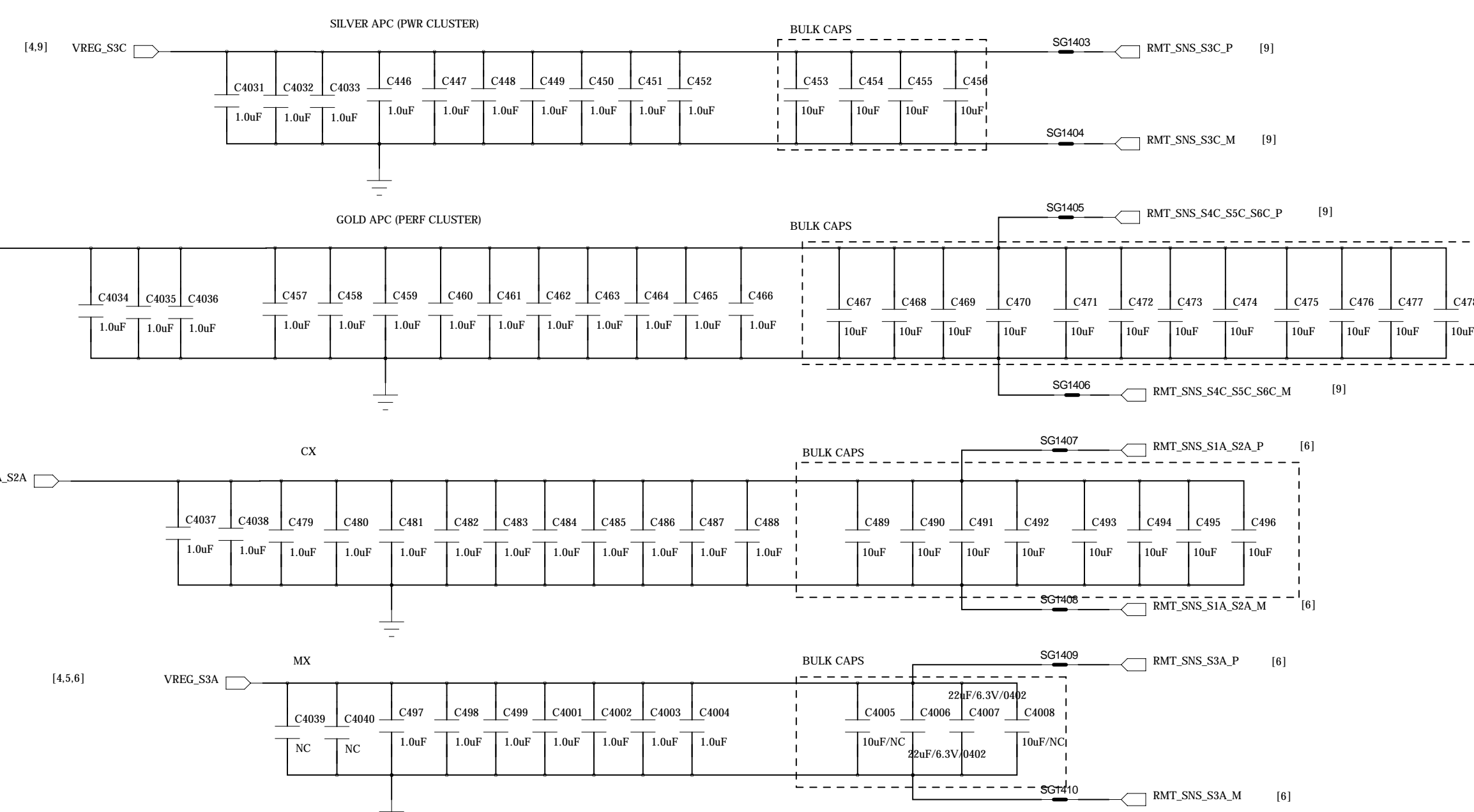
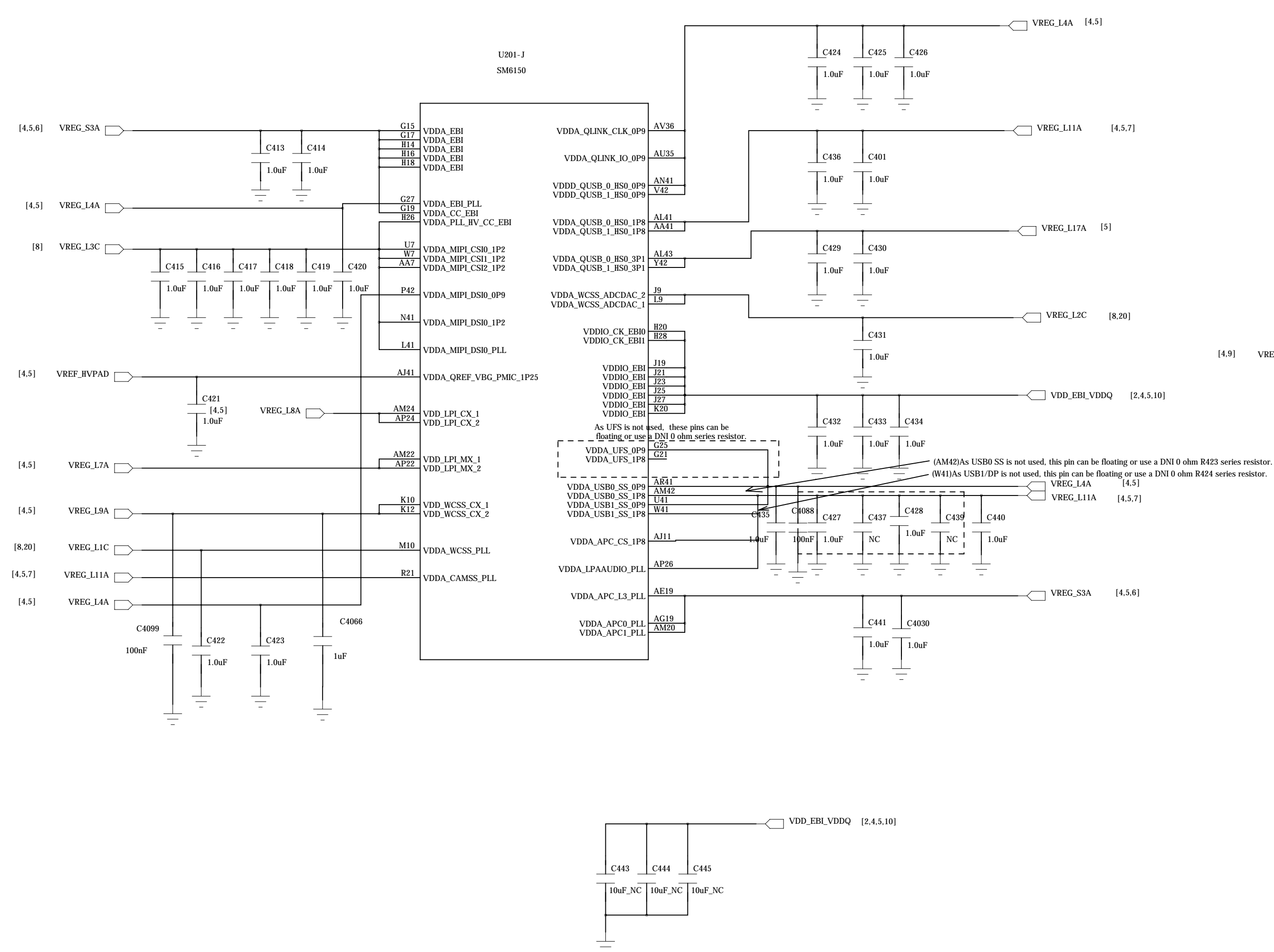
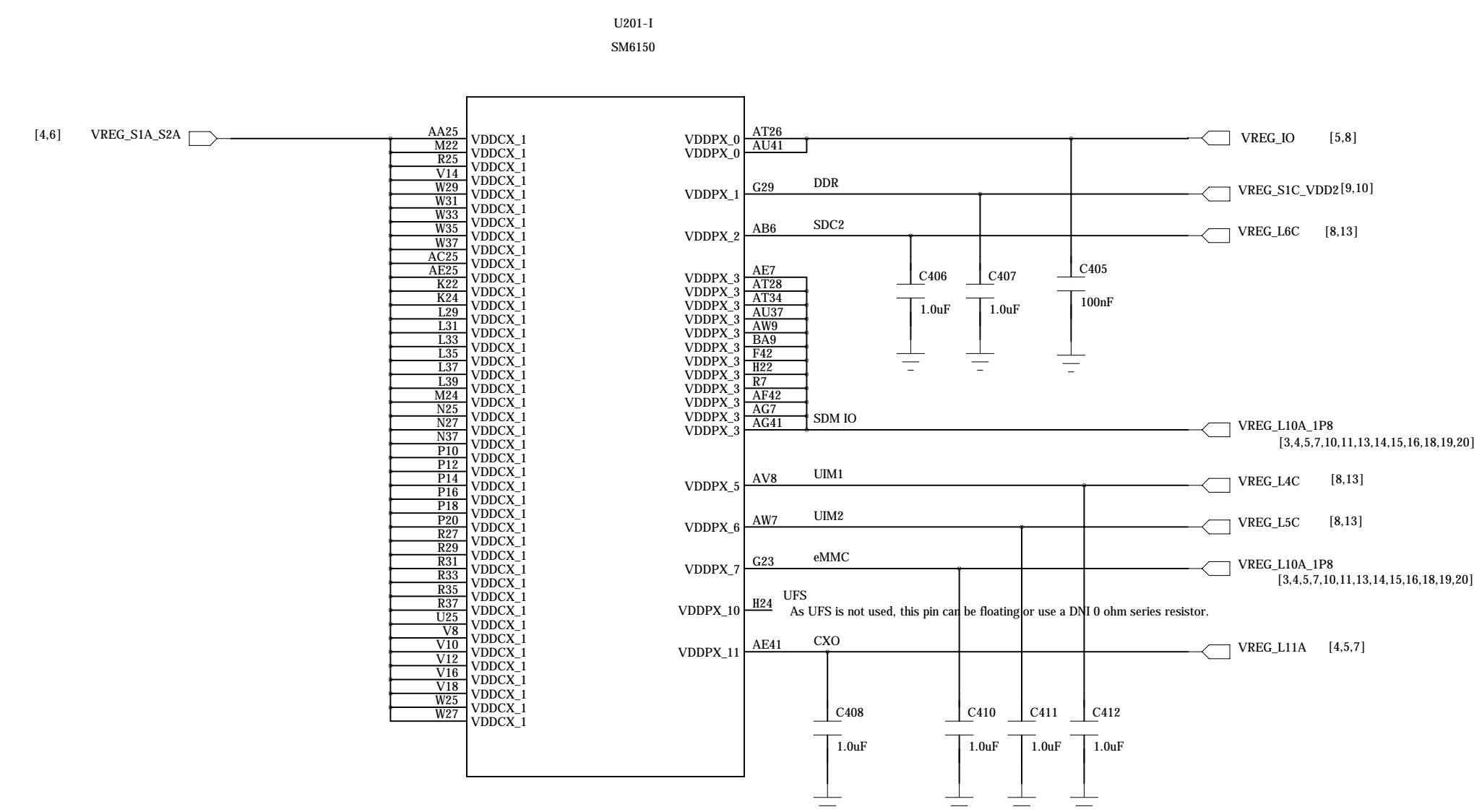
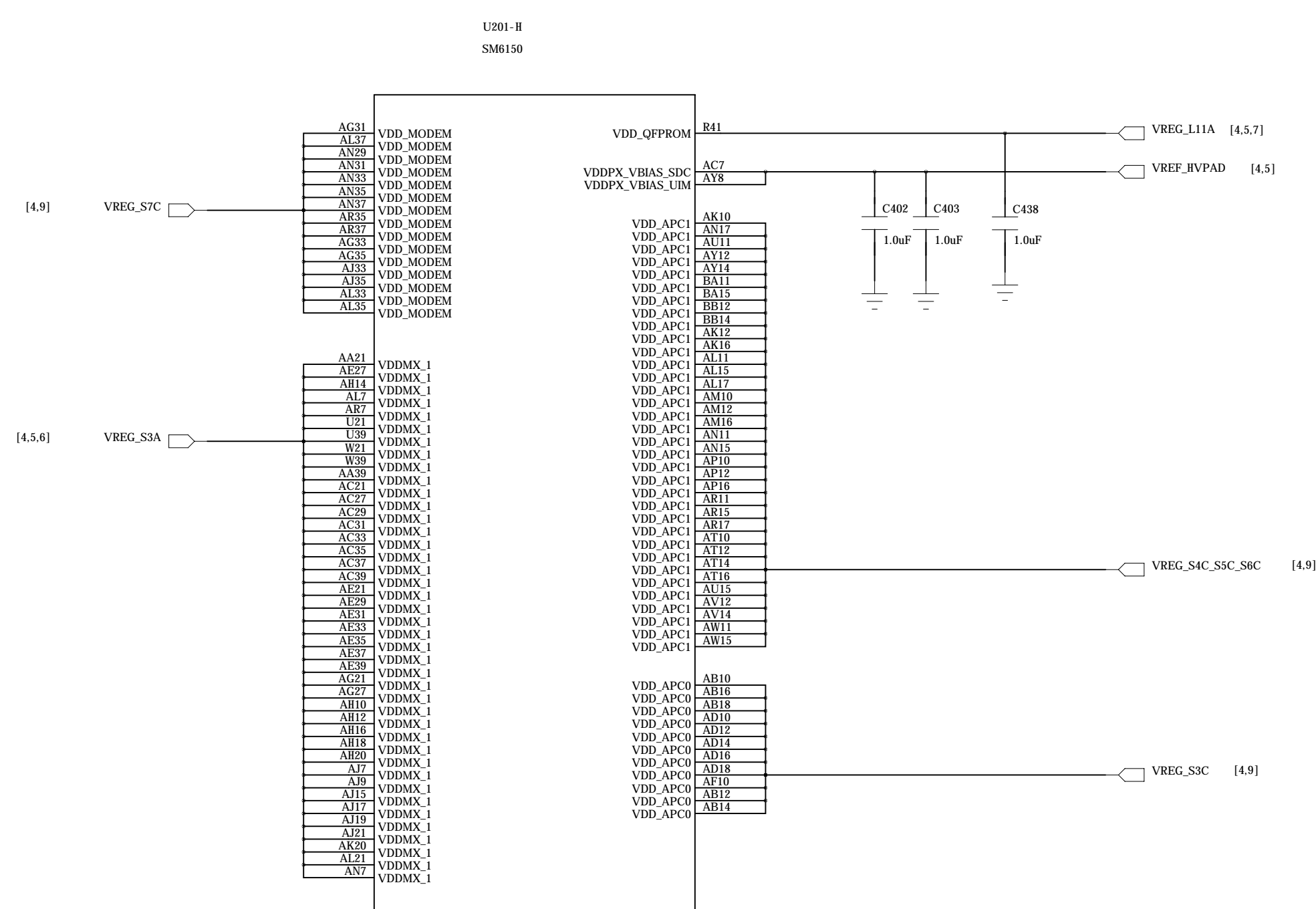


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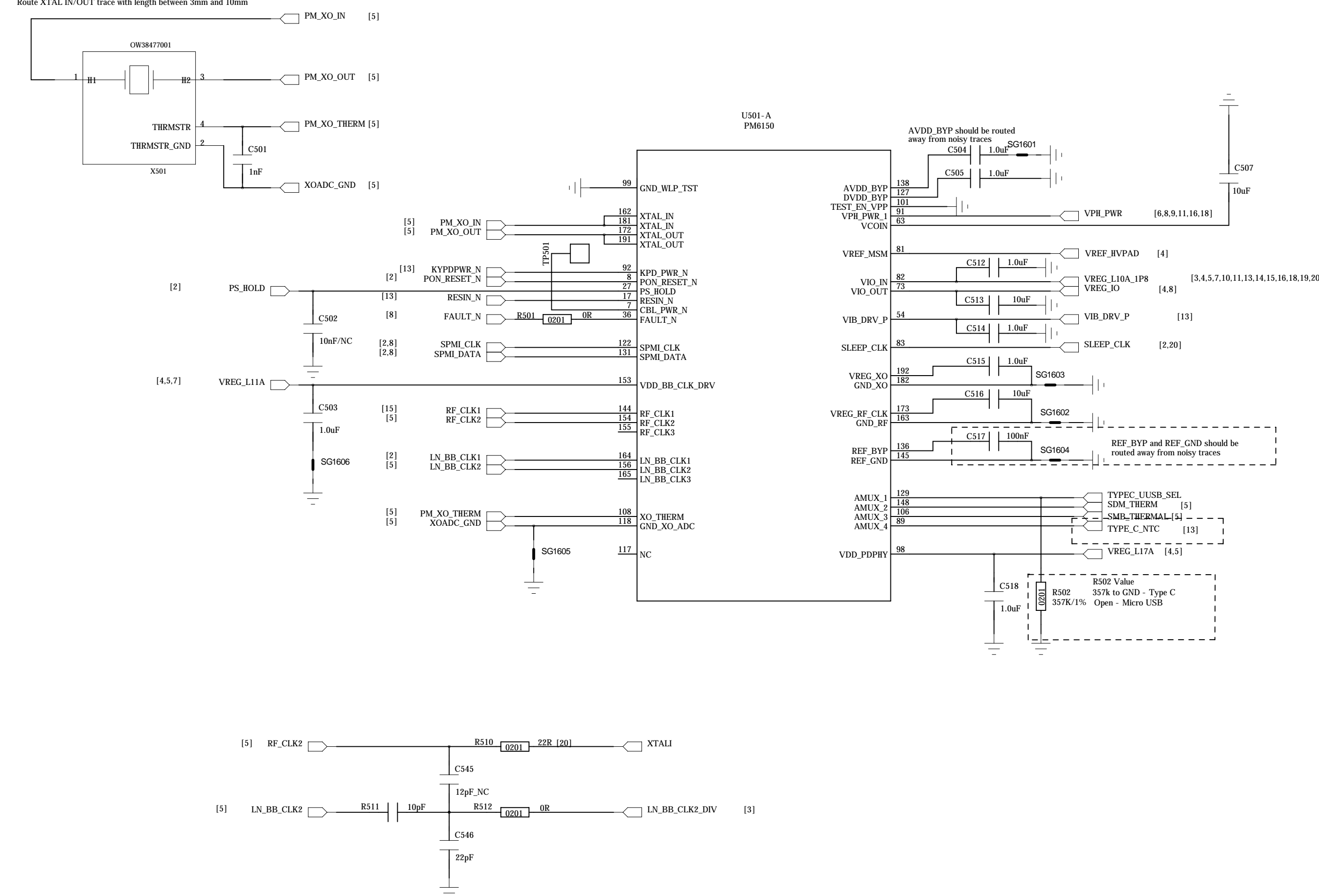


SM6150 DECAPs

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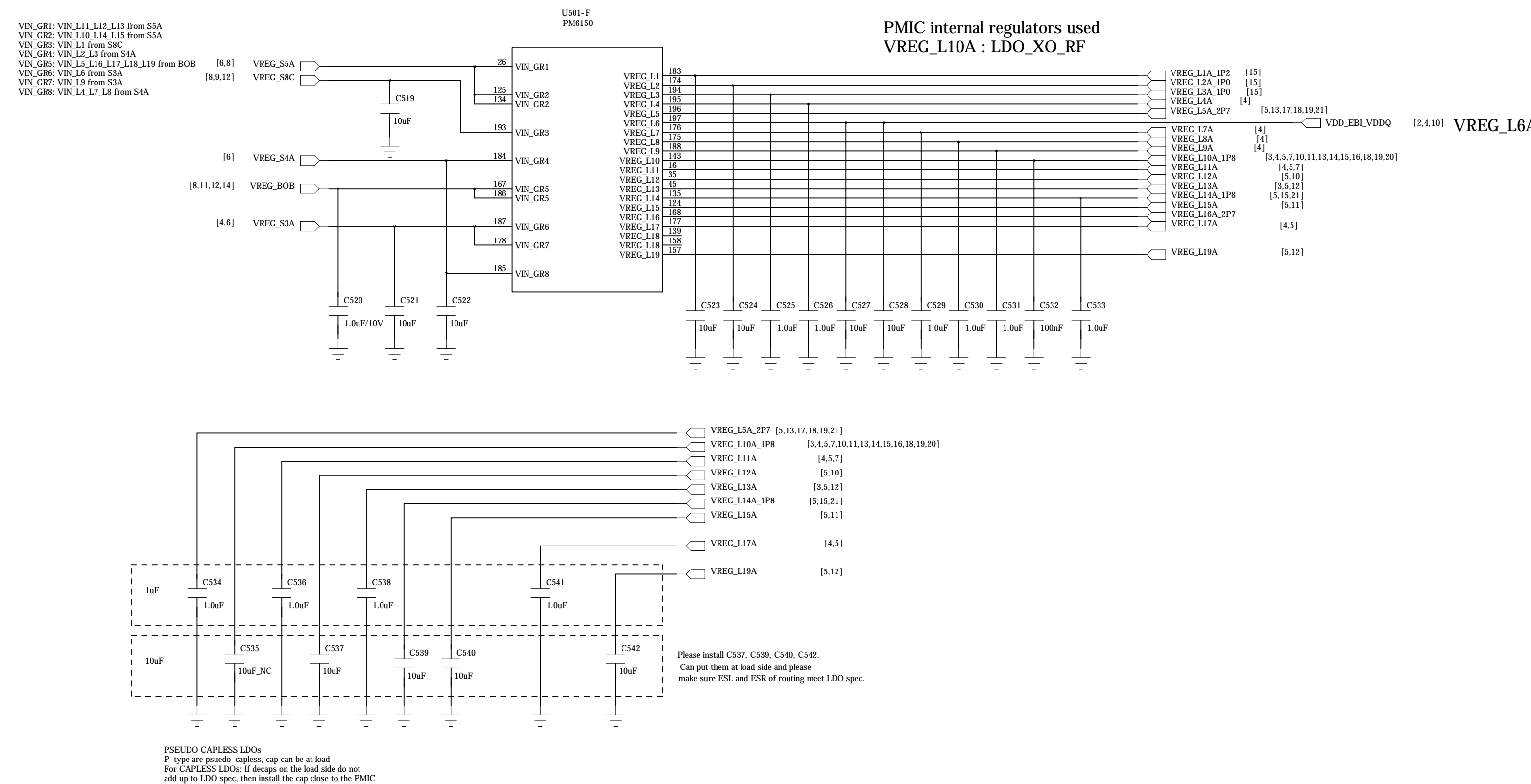
Layout Note:

No trace shall be present in at least two immediate layers below XTA

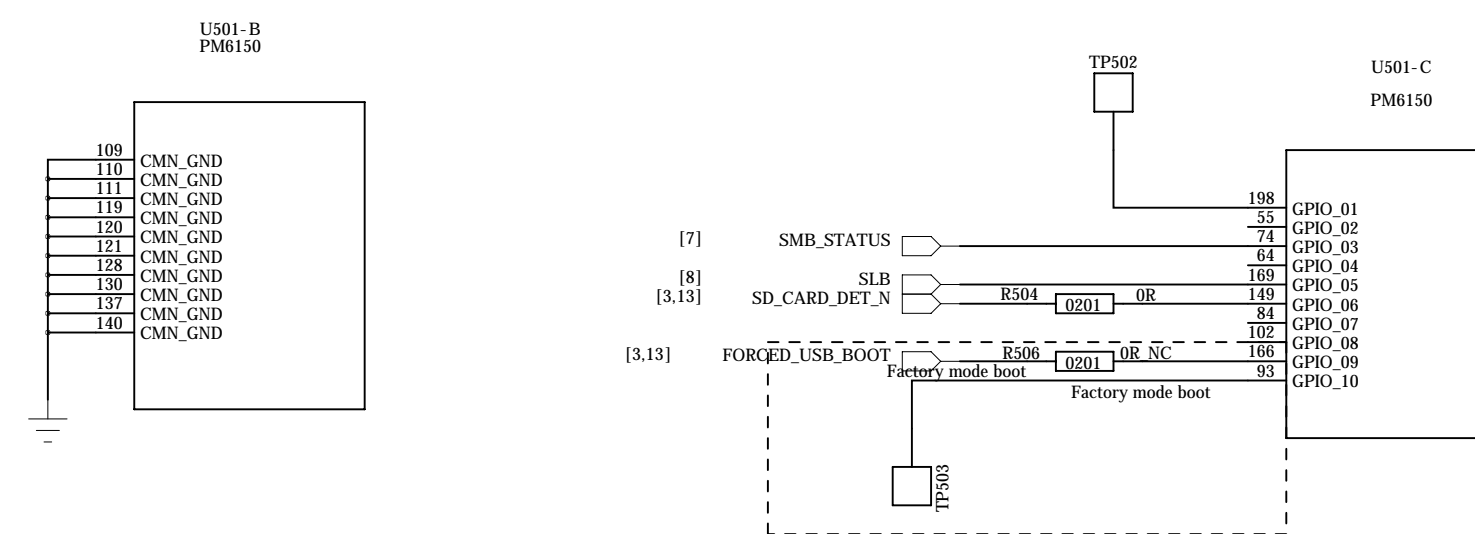
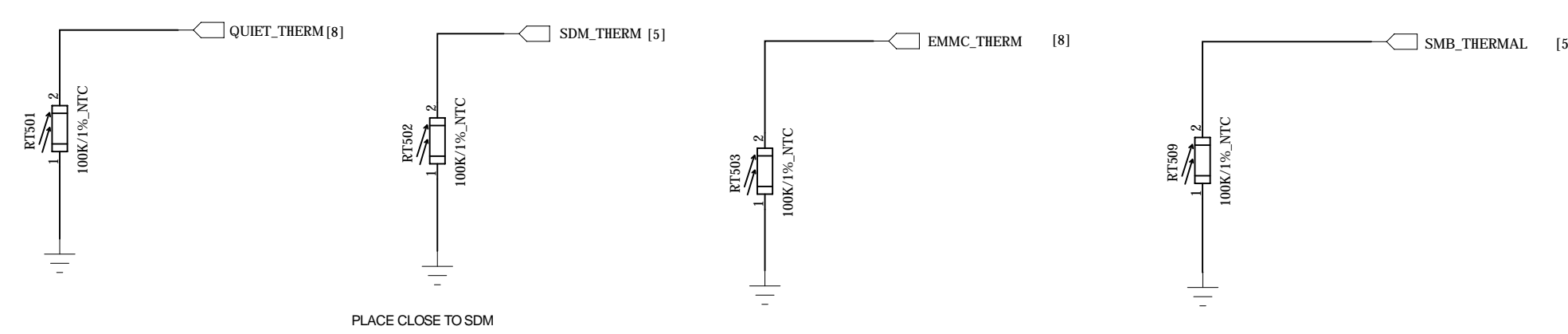


Three compatible designs are provided for SDM_RF_XO_CLK.

1. WLAN_5G_CLK_OUT:
R511/C545/C546/R512/R302 are DNI
2. Divided CLK of RFCLK2(LN_BB_CLK2_DIV):
R511/R303/C2027/C2005/C2006 are DNI and SW needs to ban WLAN_5G_CLK_OUT
3. LN_BB_CLK2 (Default)
C545/R303/C2027/C2005/C2006 are DNI and SW needs to ban WLAN_5G_CLK_OUT
R511 and C546 as a divider is following IDP design.

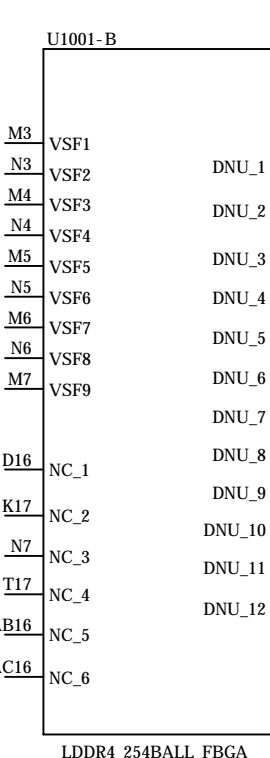


PSEUDO CAPLESS LDOs
P-type are pseudo-capless, cap can be at load
For CAPLESS LDOs: If decaps on the load side do not
add up to LDO spec, then install the cap close to the PMIC



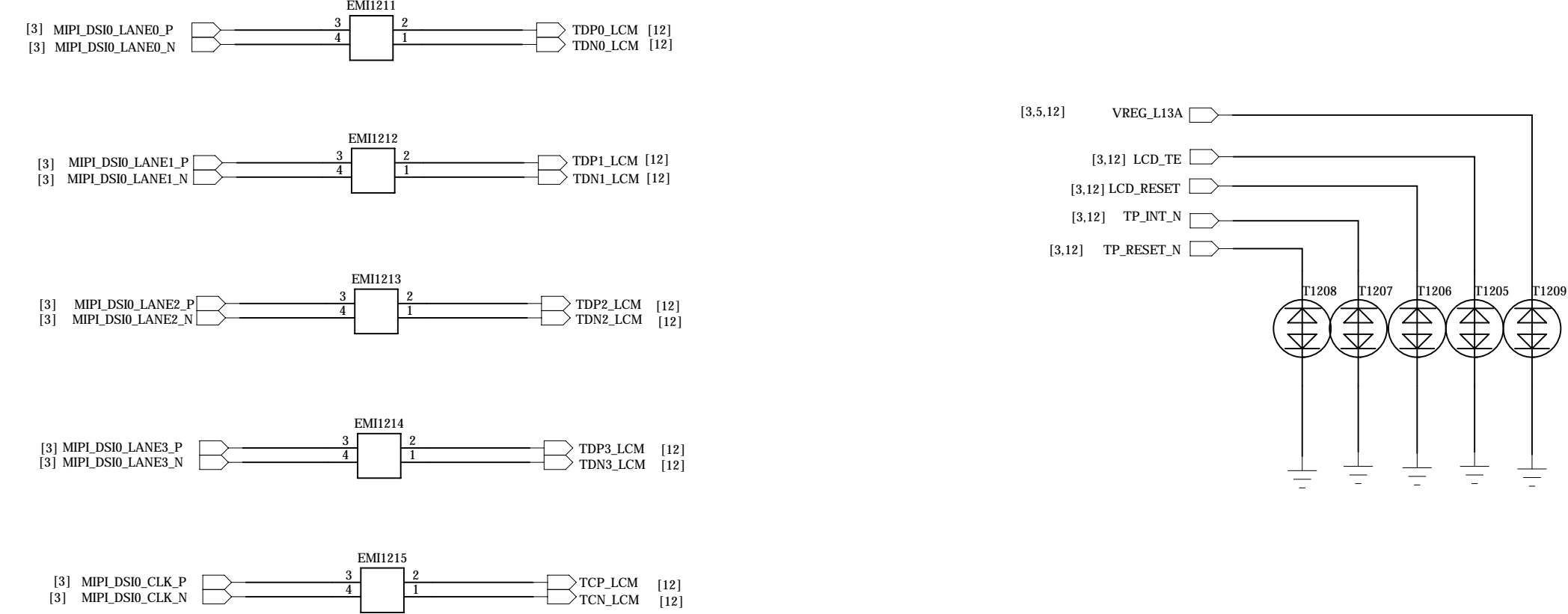
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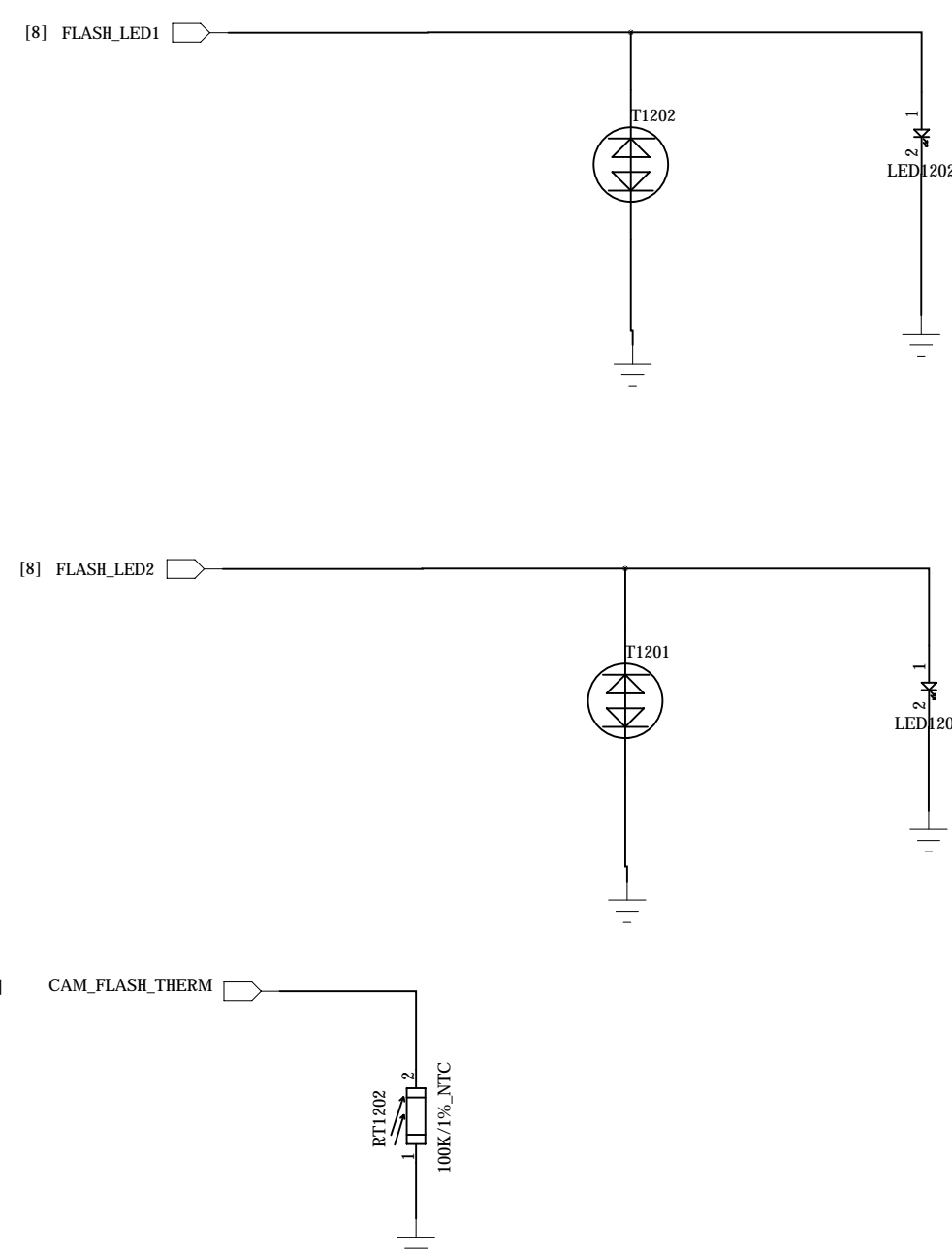


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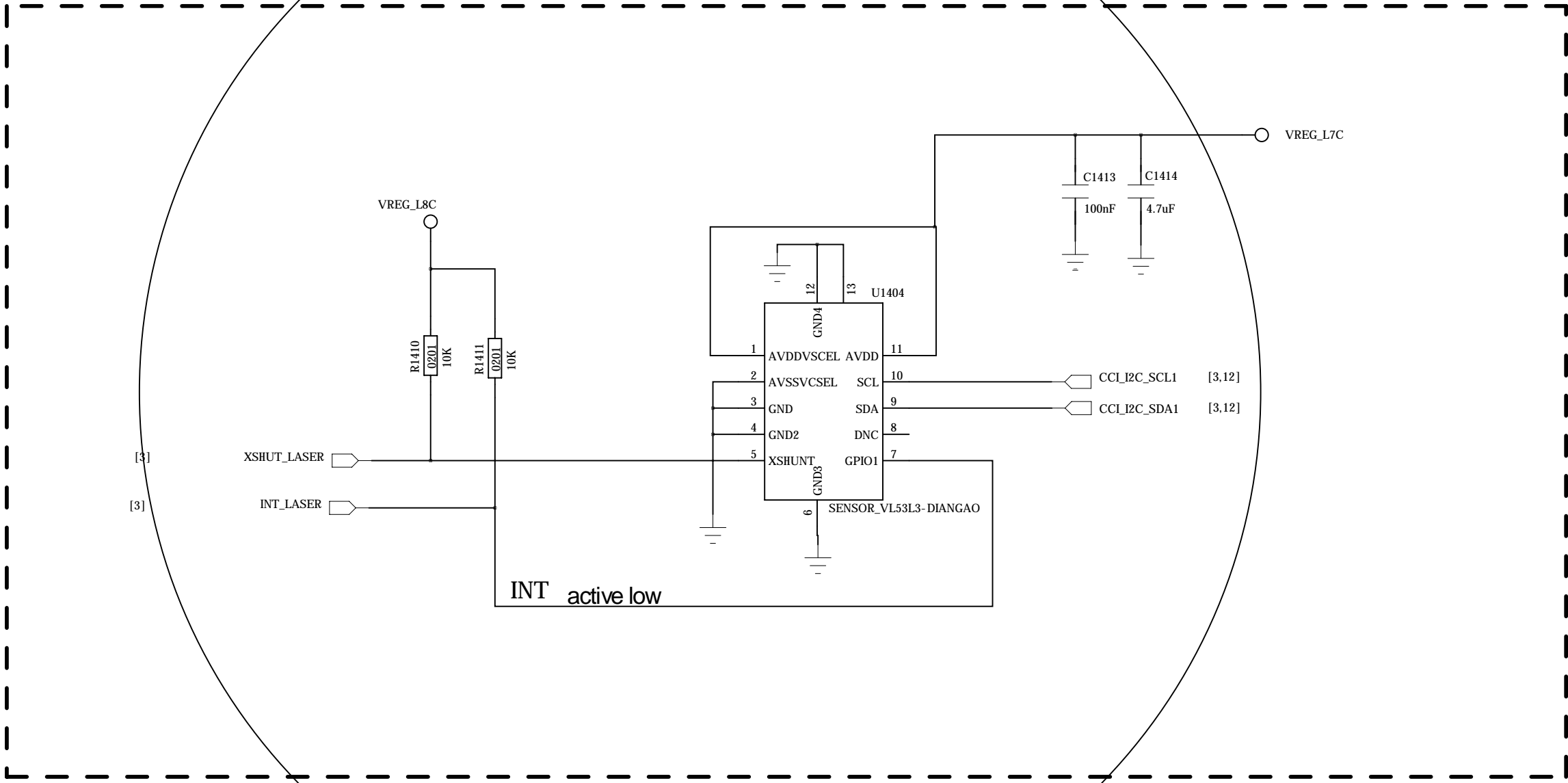
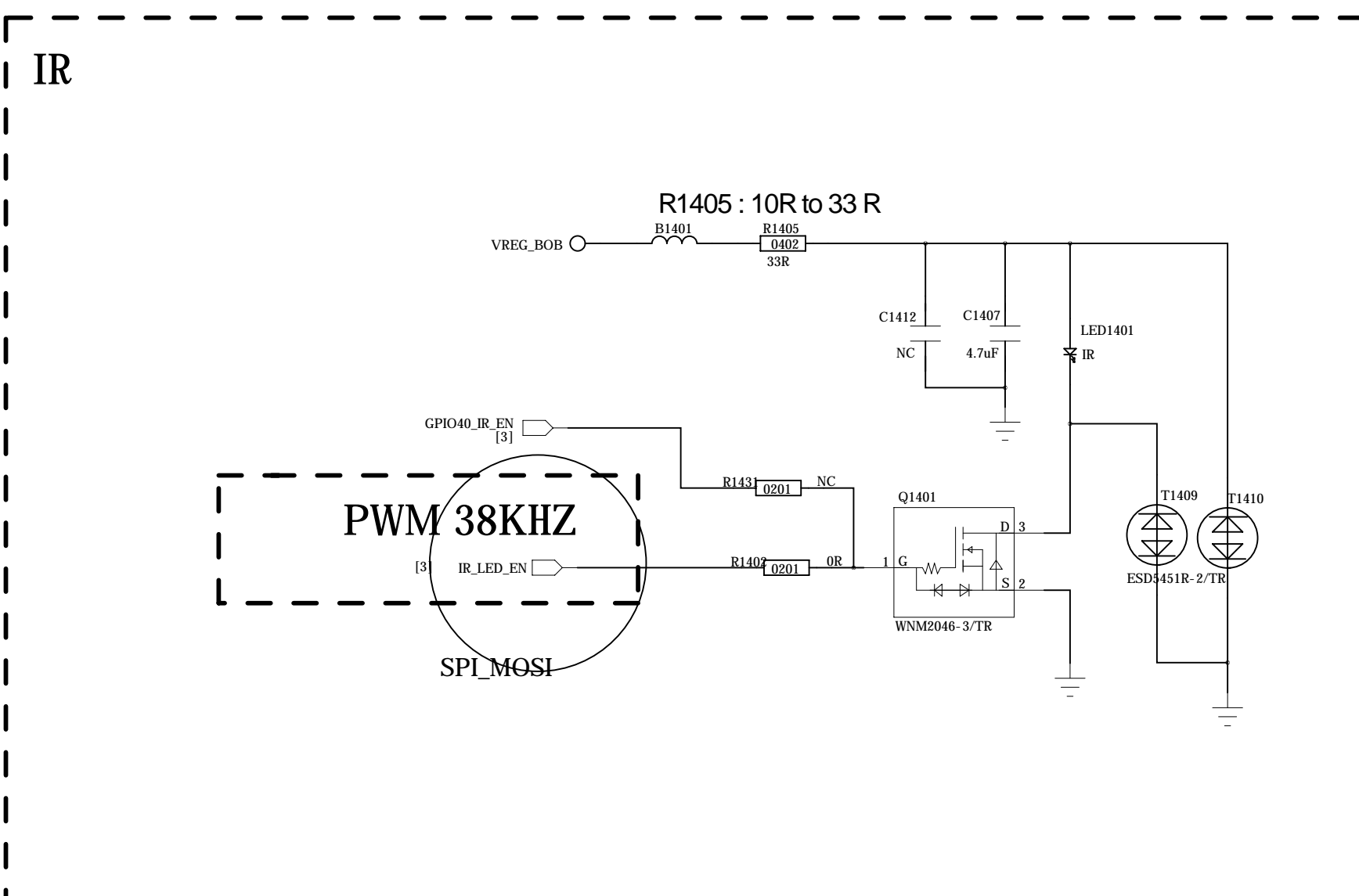
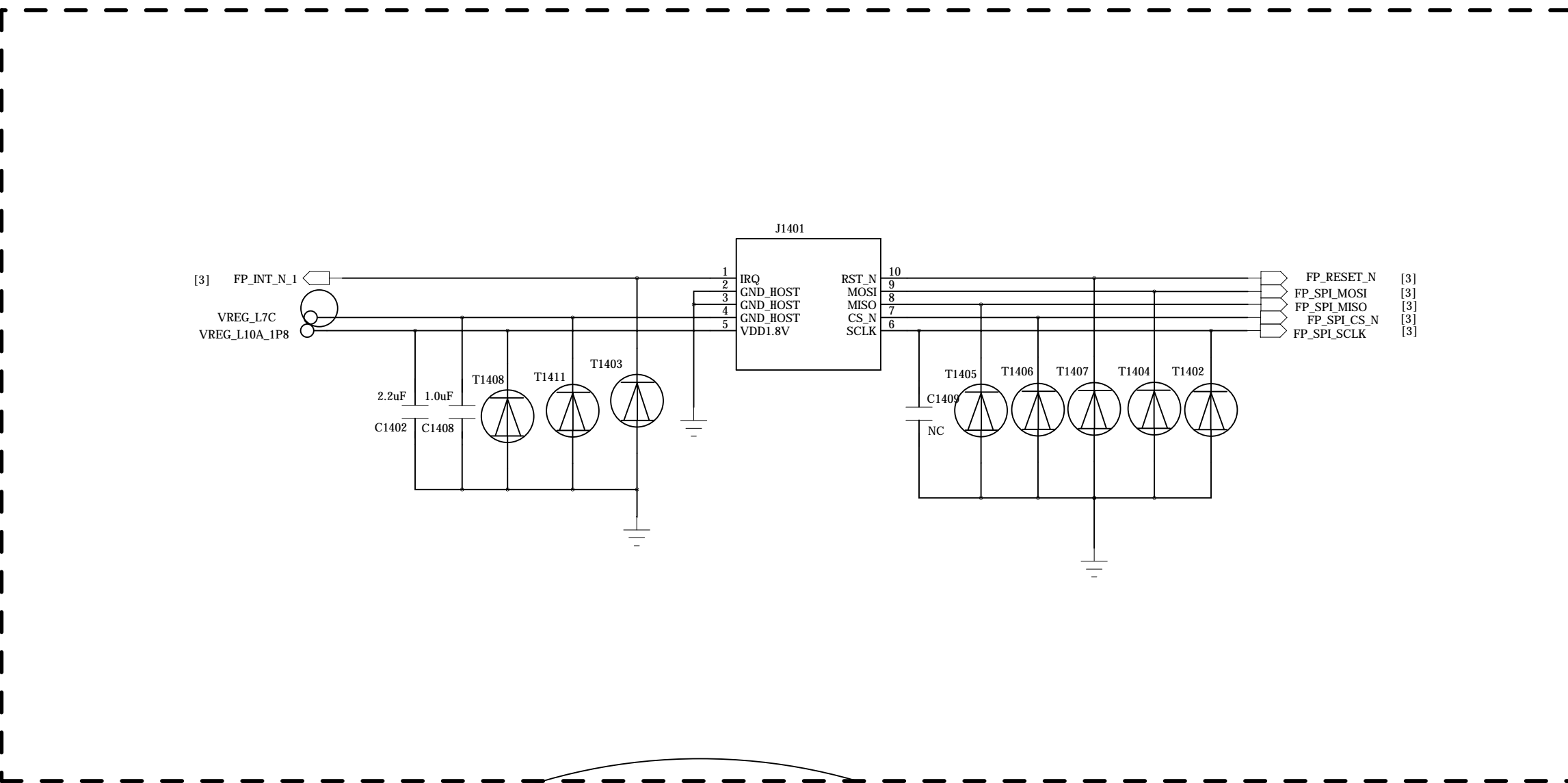
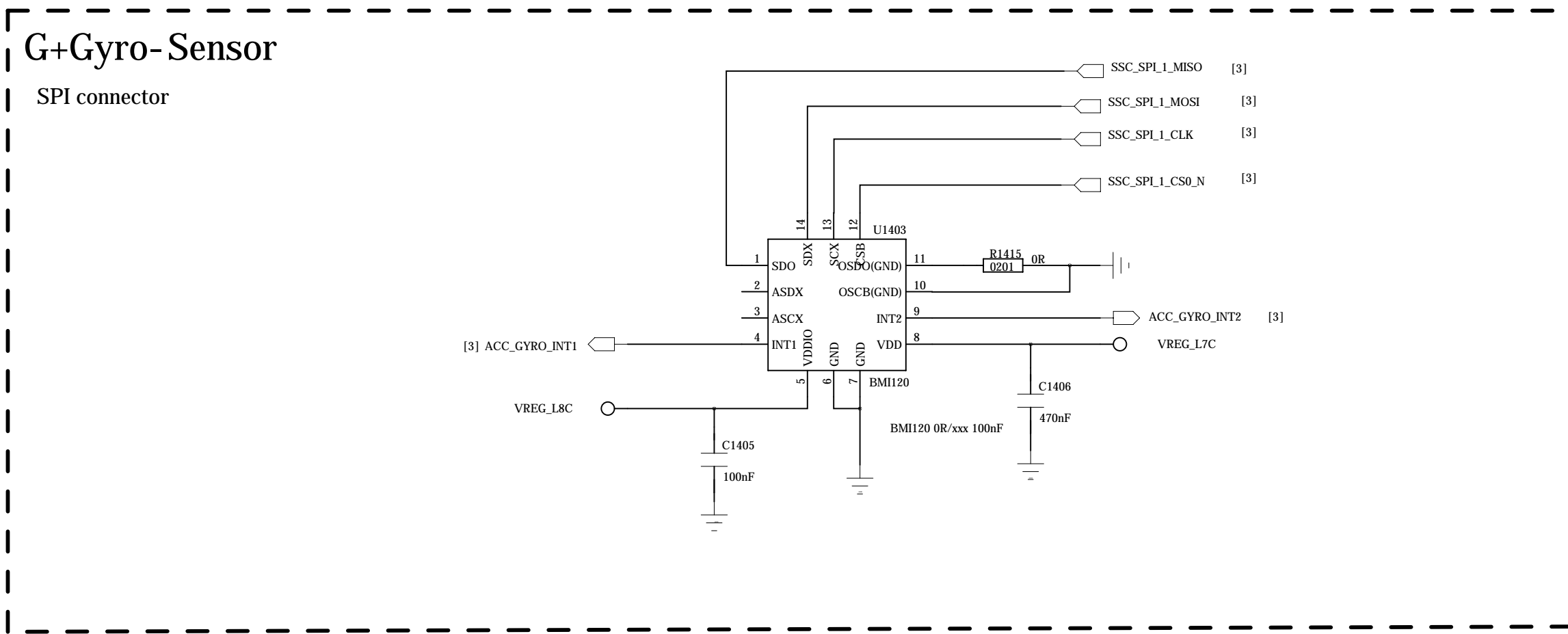
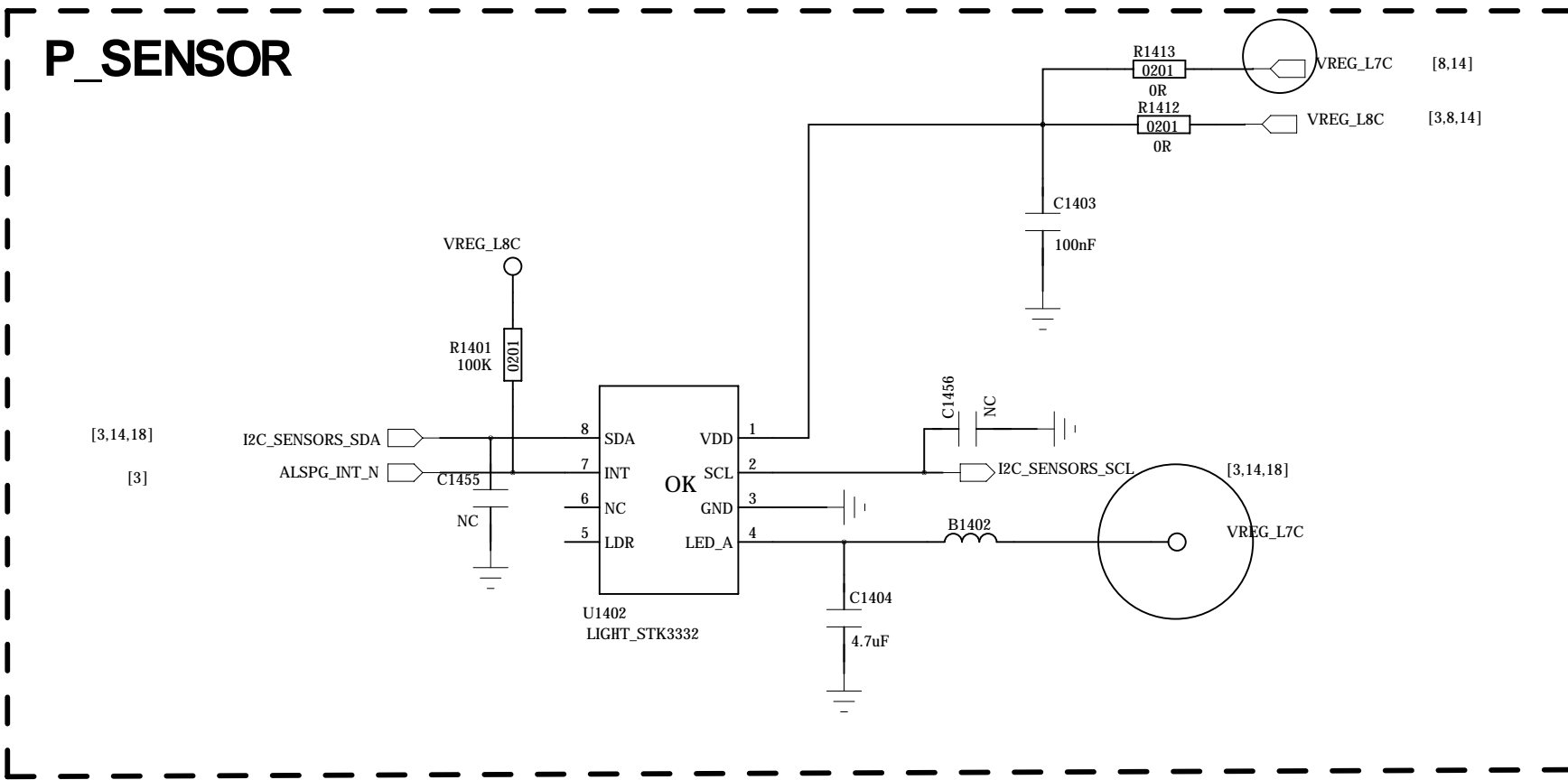
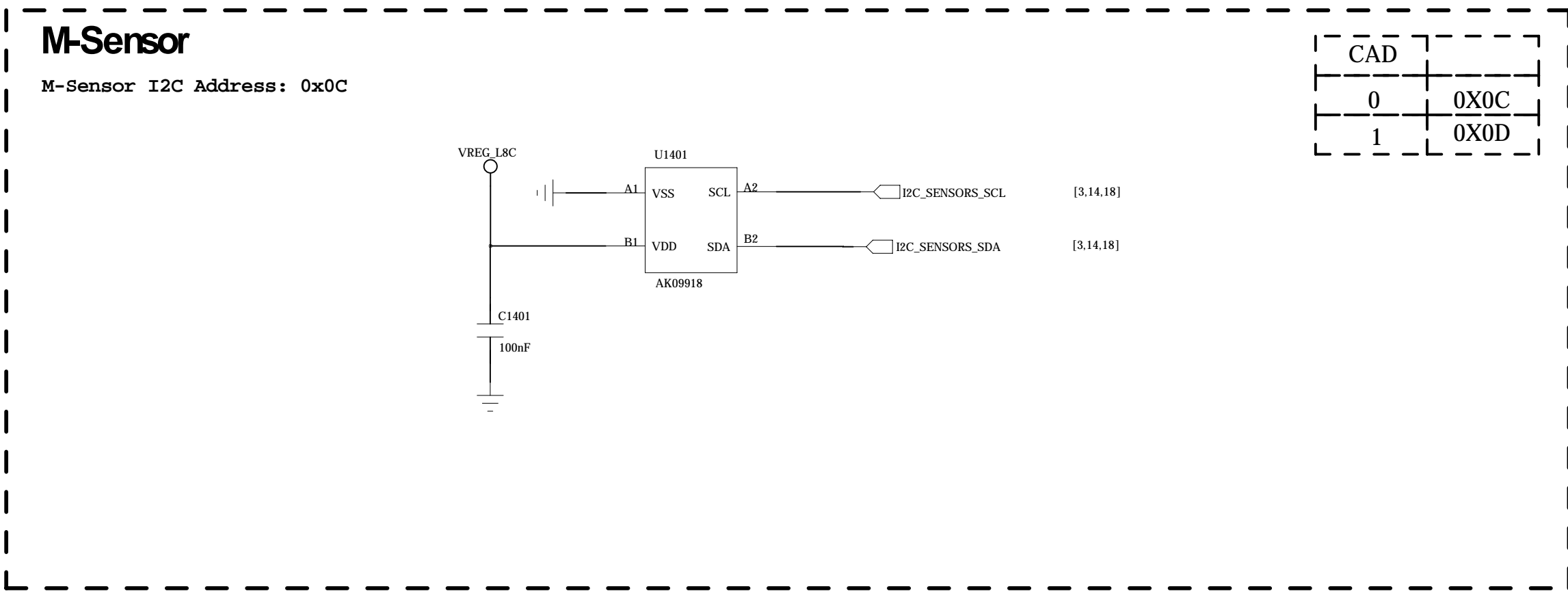


Flash Light



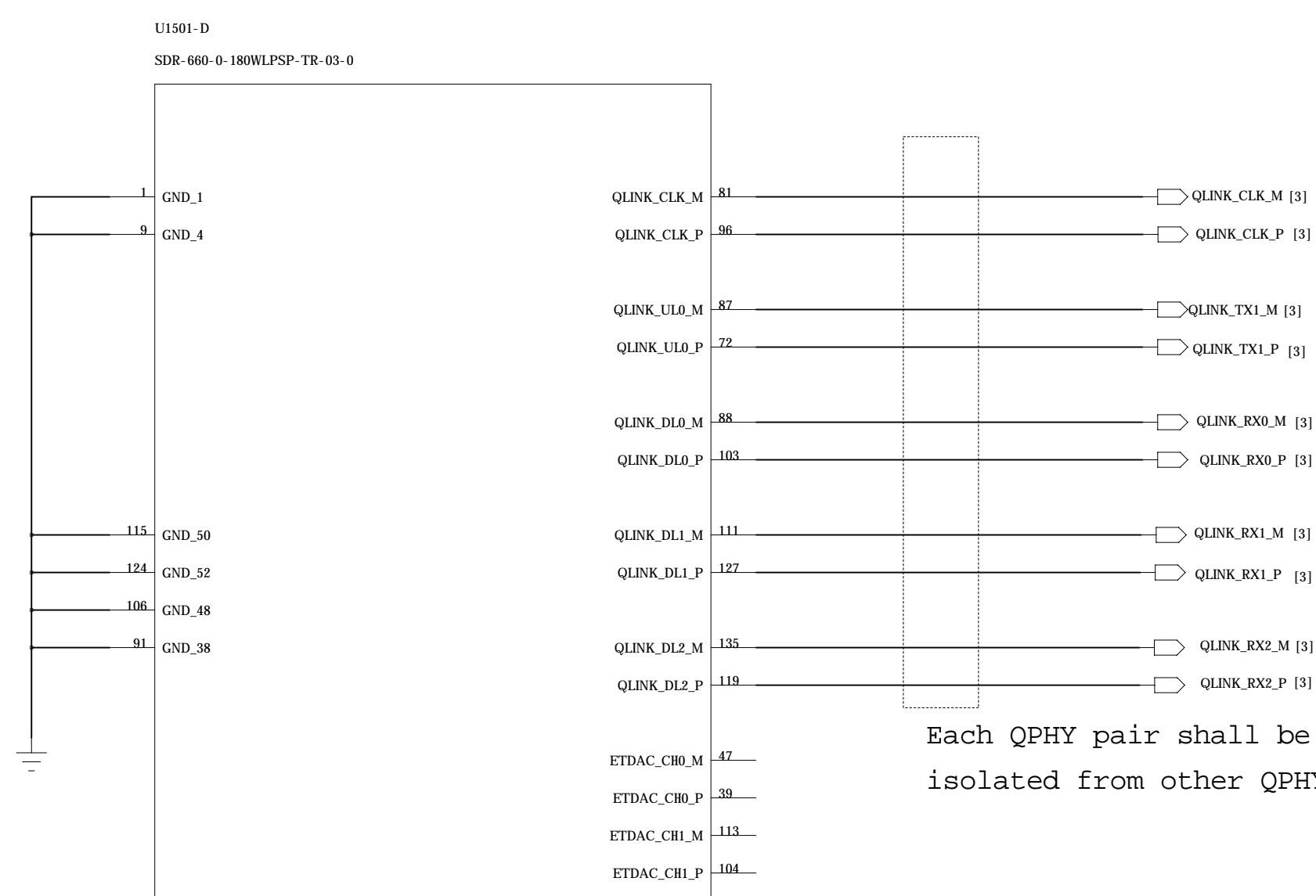
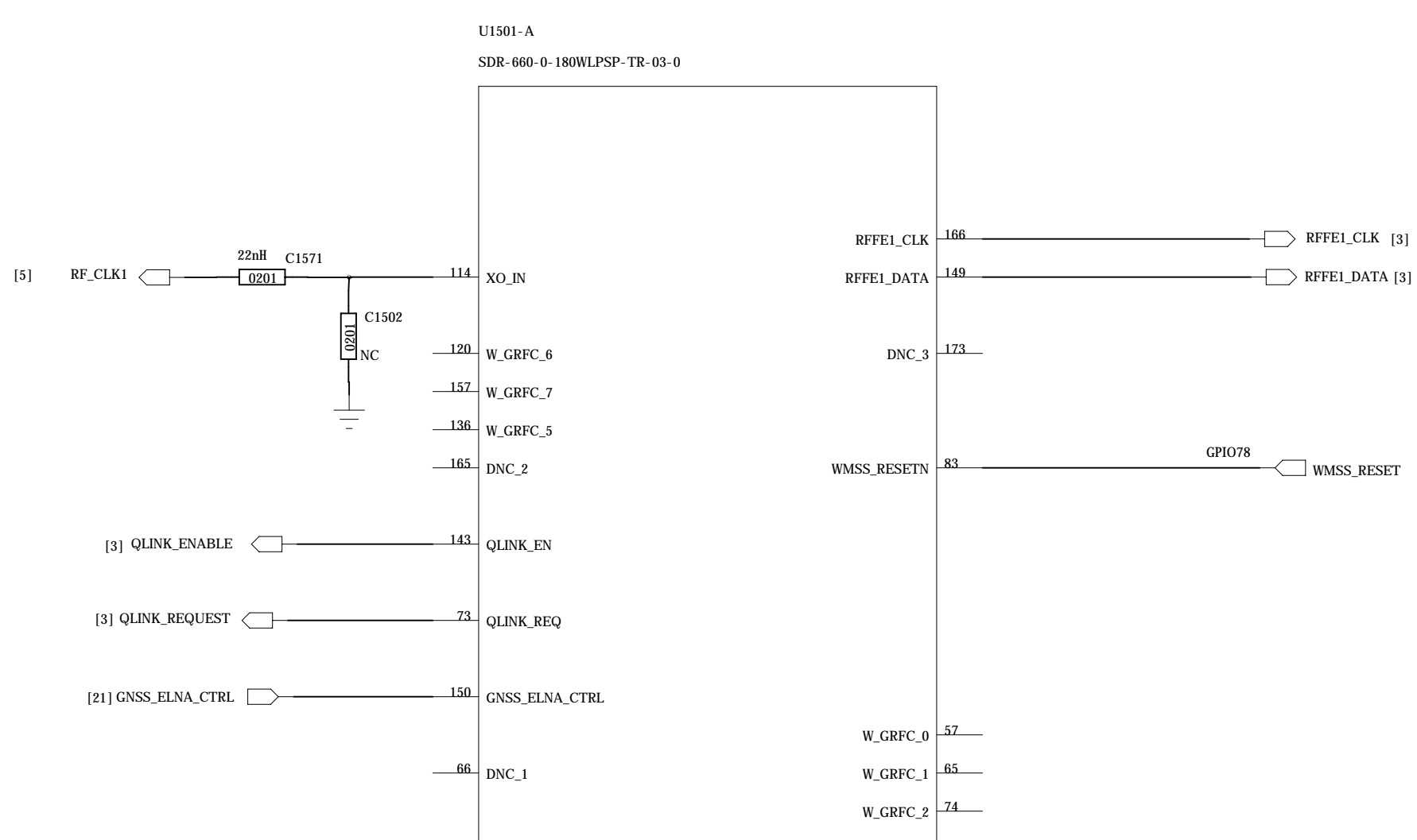
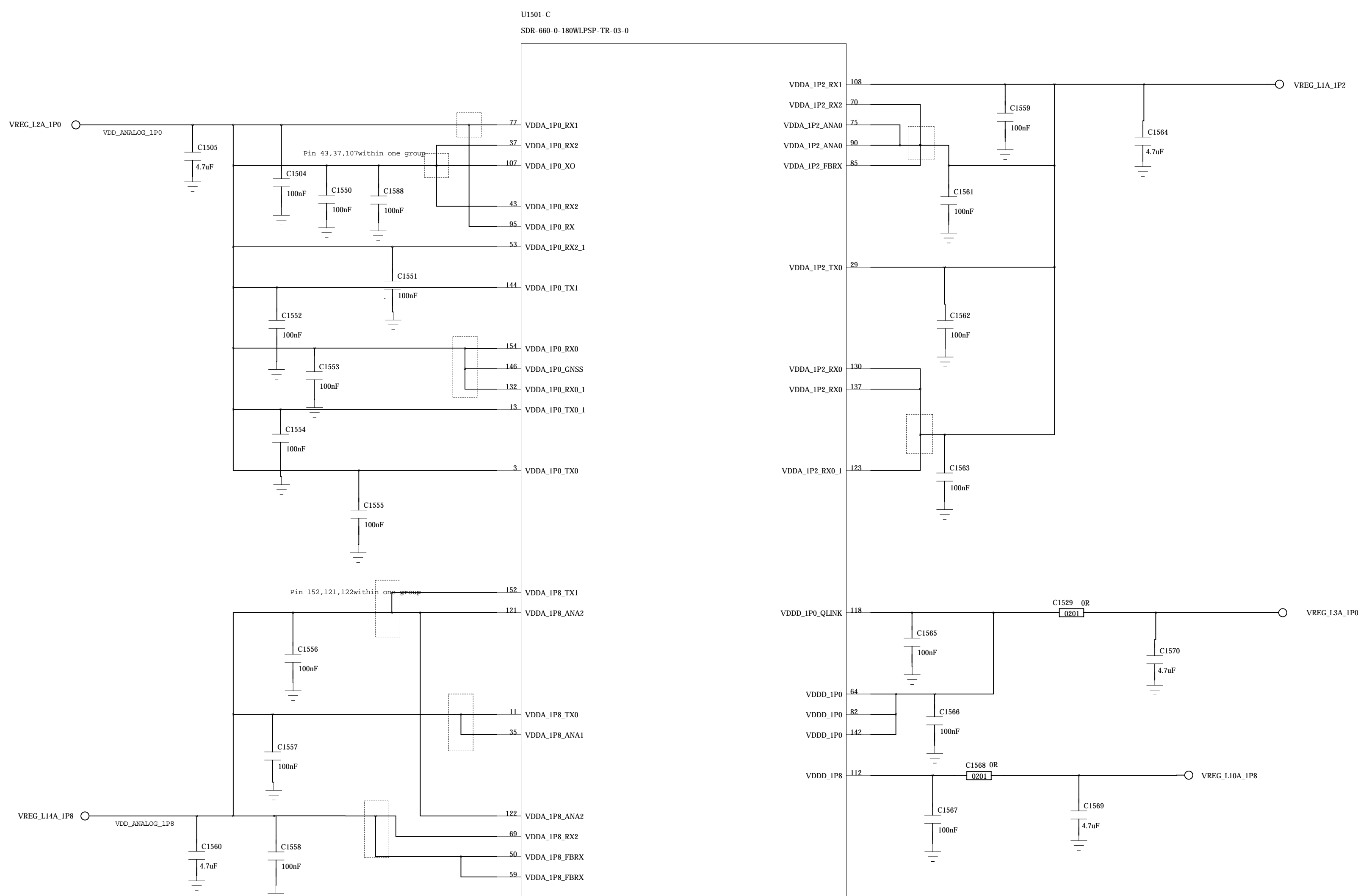
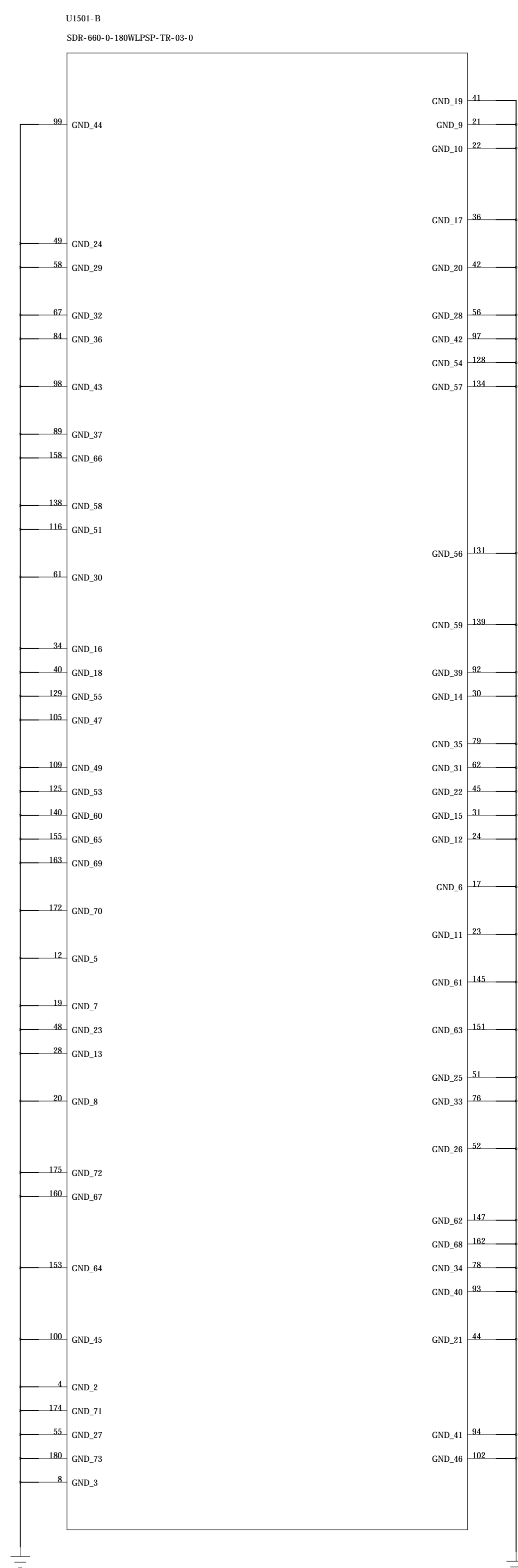
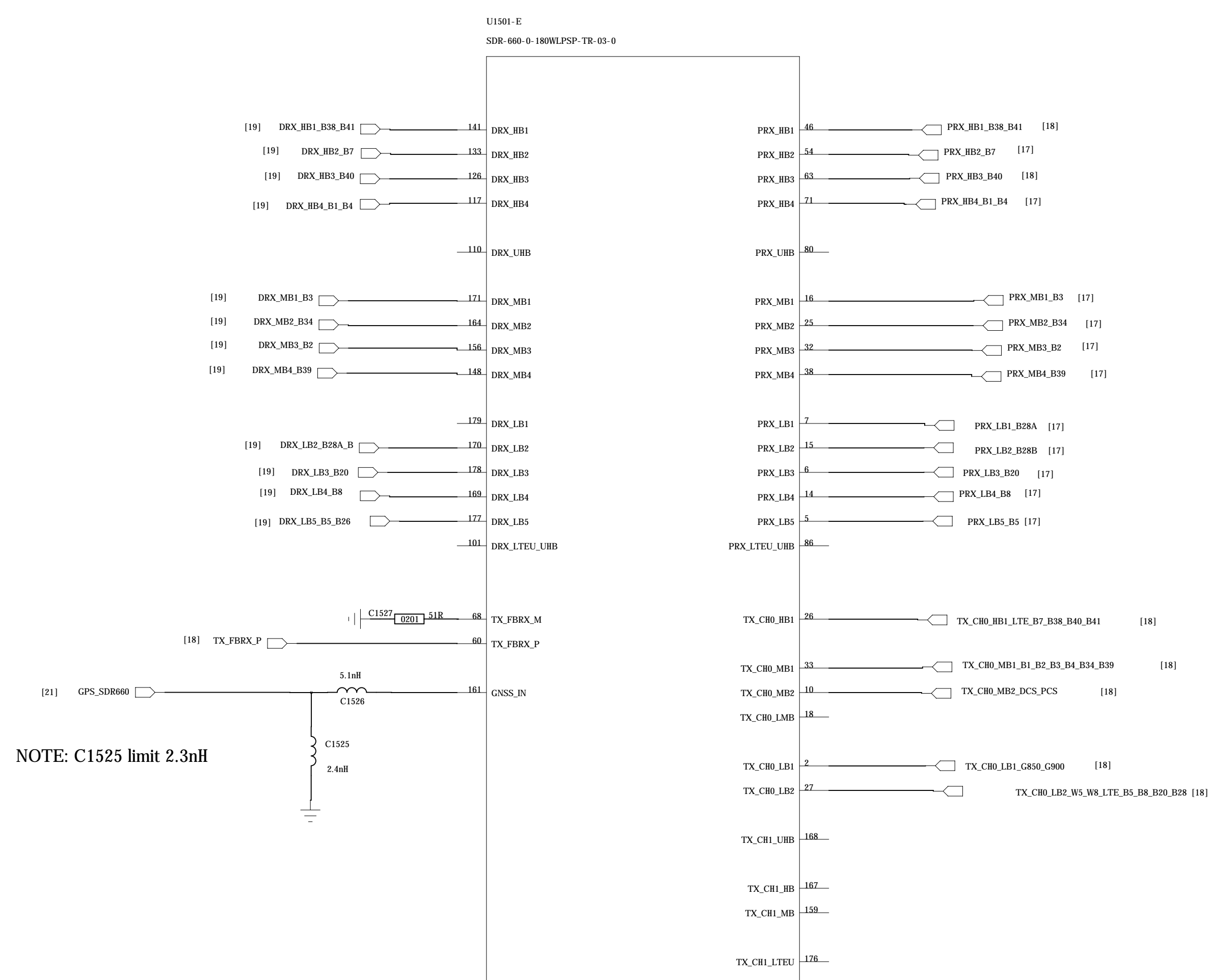
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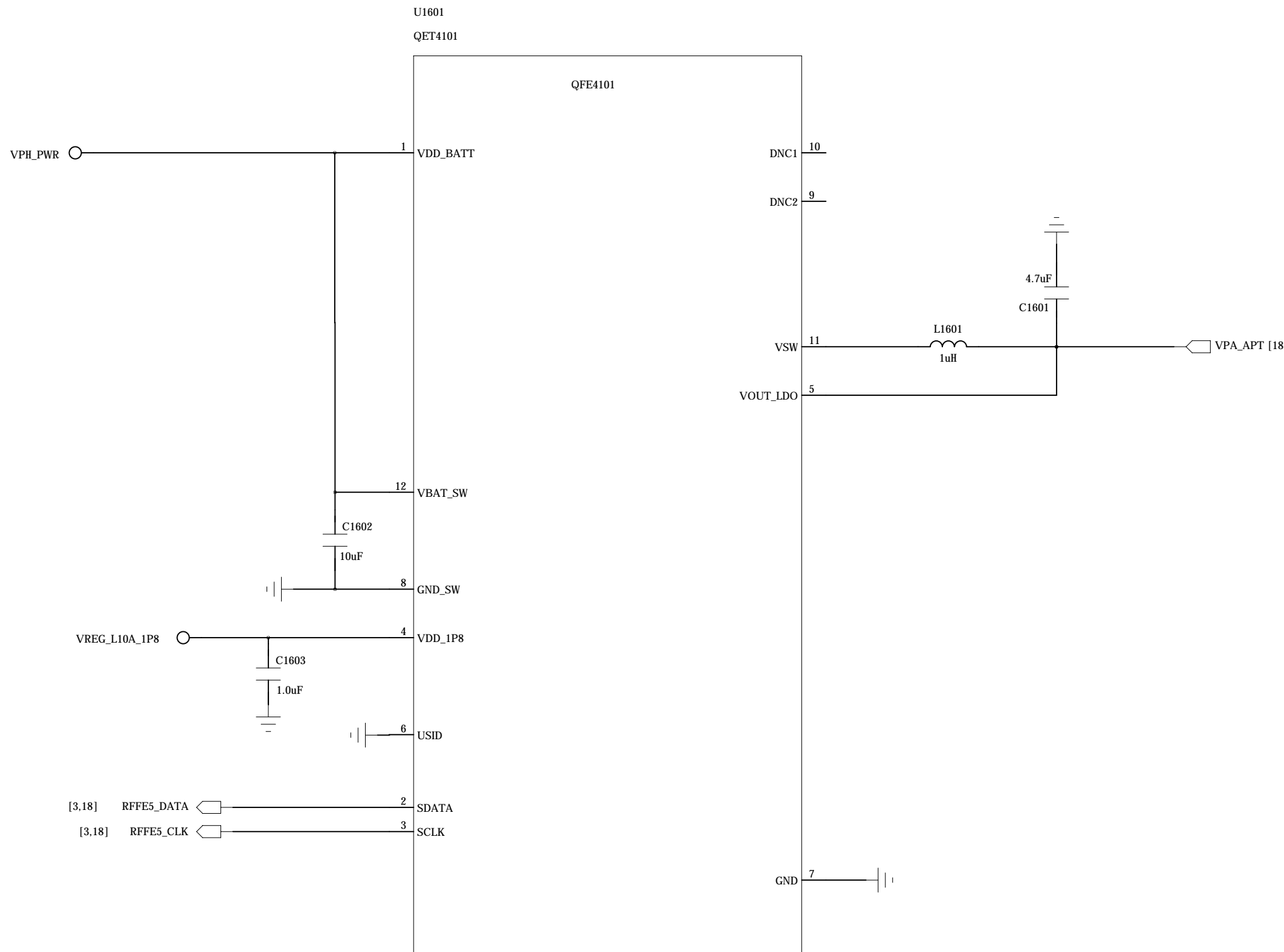


Each QPHY pair shall be routed in a channel together isolated from other QPHY pairs with surrounding gnd

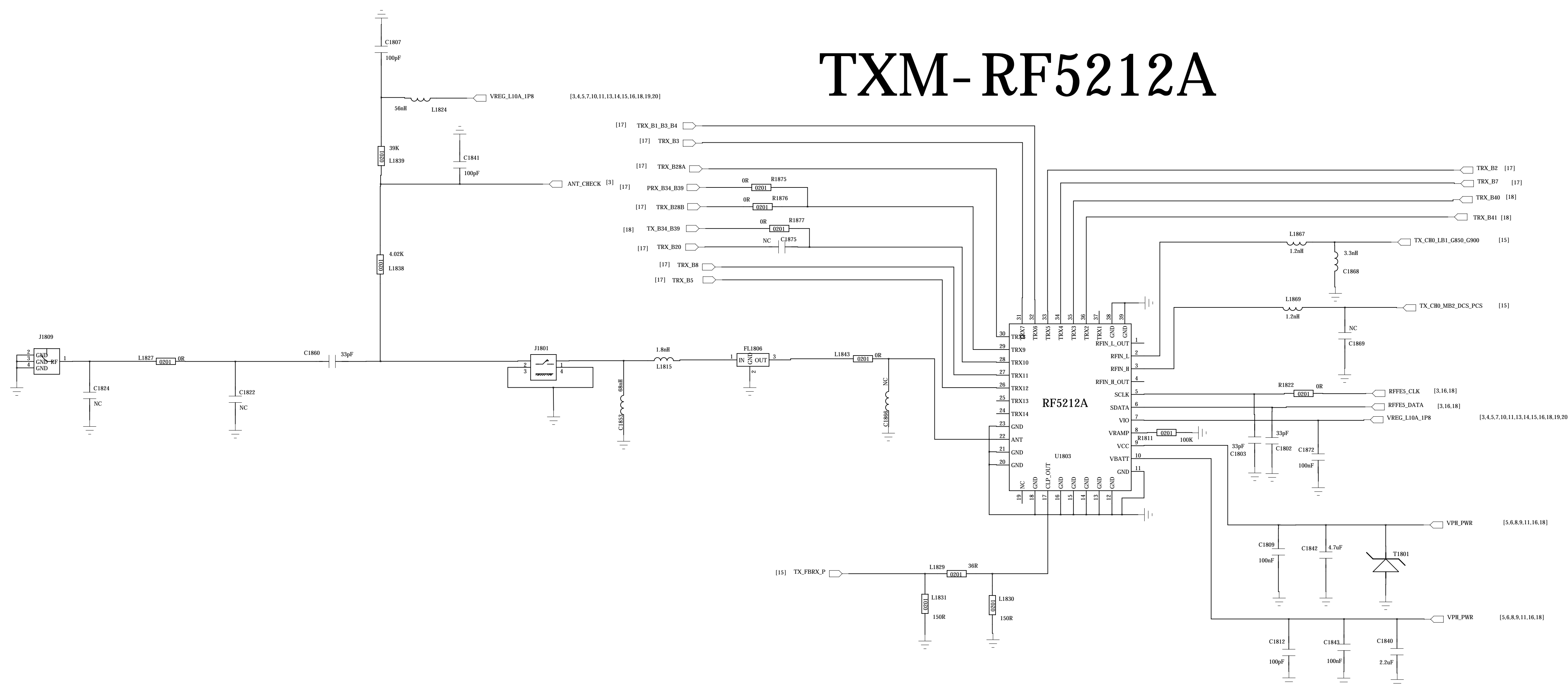
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TXM-RF5212A

MMMB_PA

PRX_B41

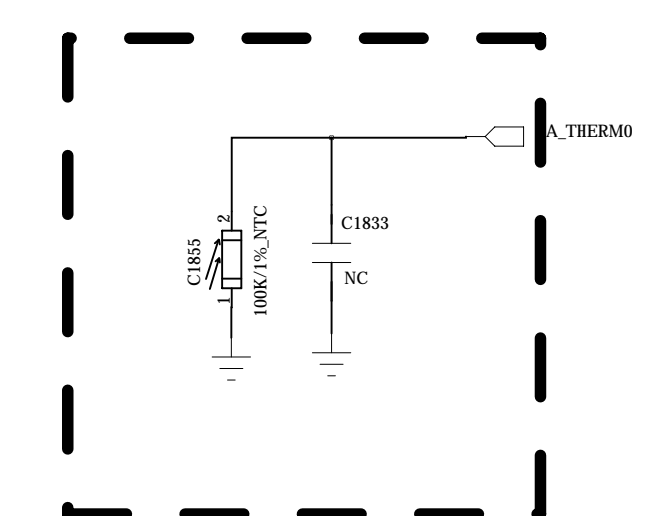
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TX_B7

TRX_B40

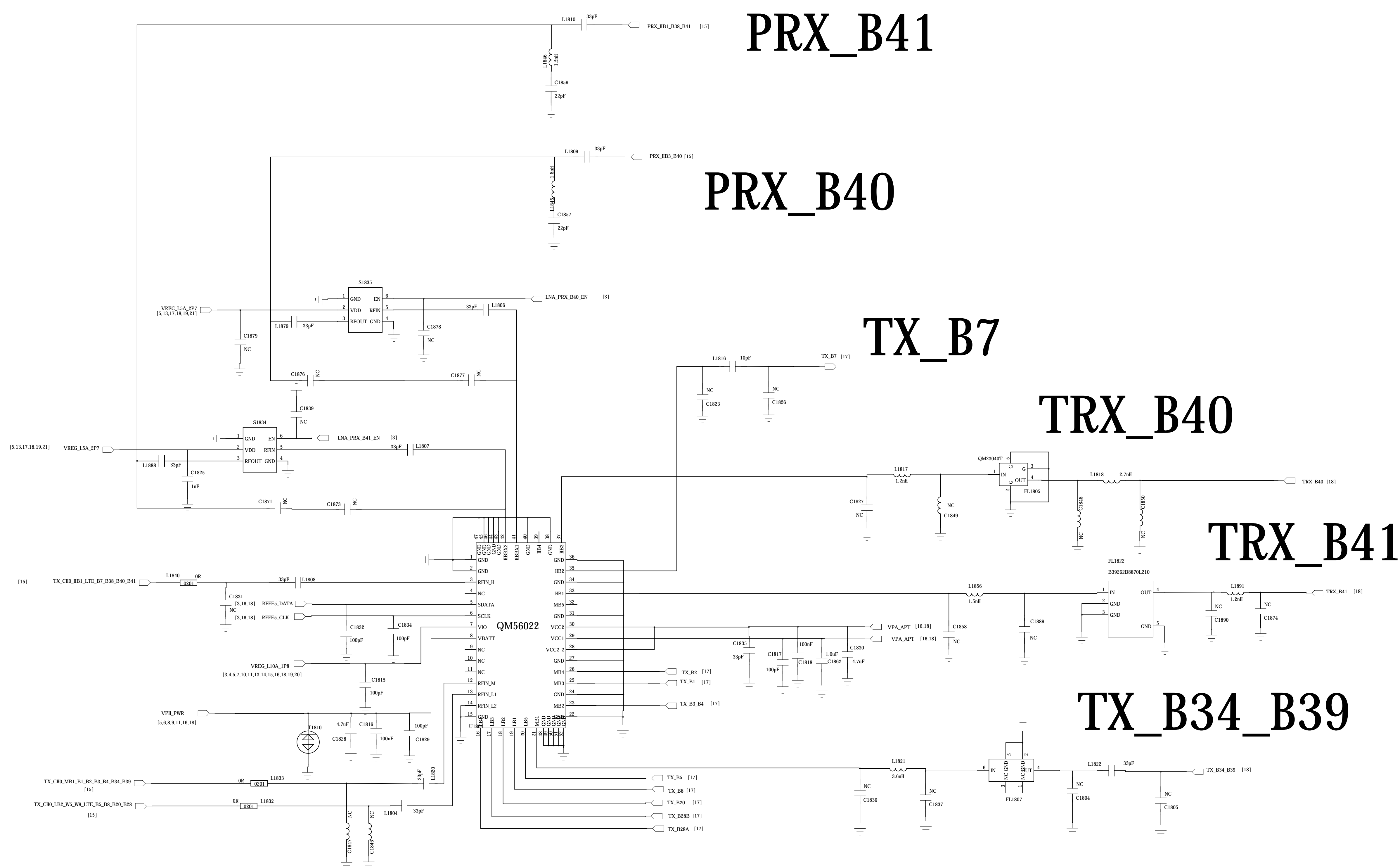
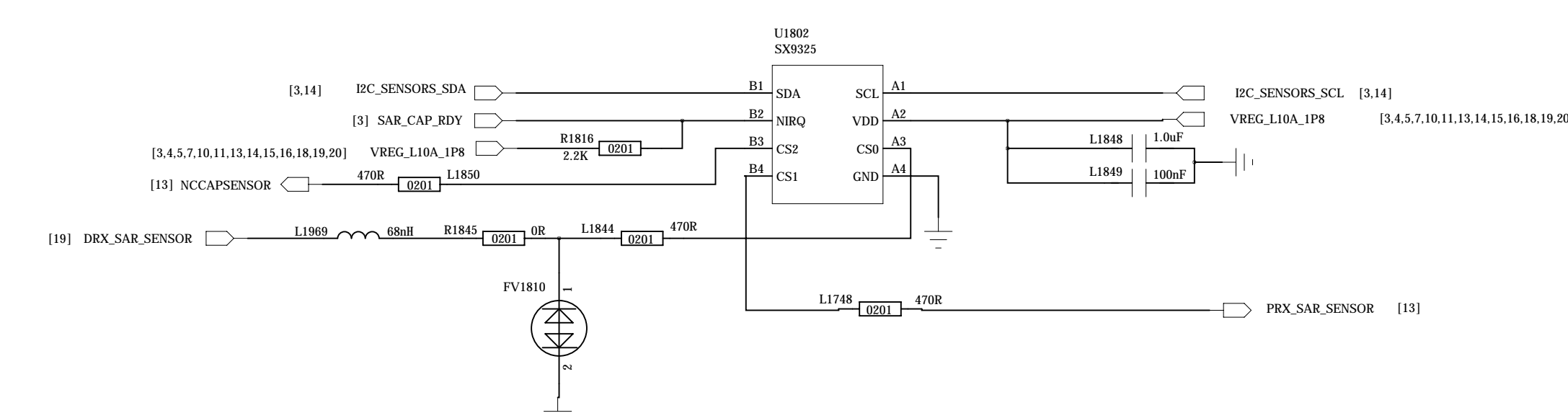
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TX B34 B39



PA_THERMO

SAR_SENSOR



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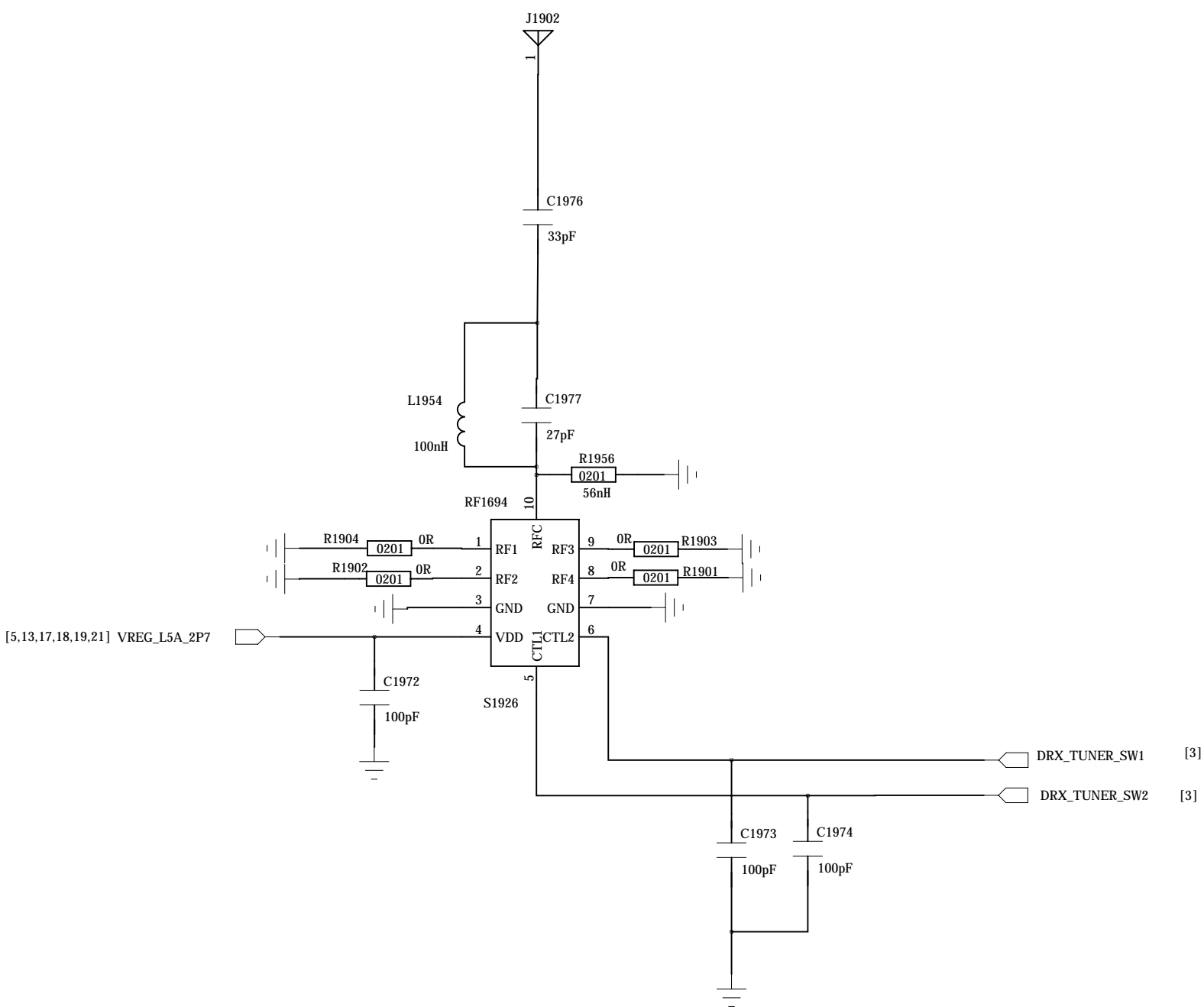
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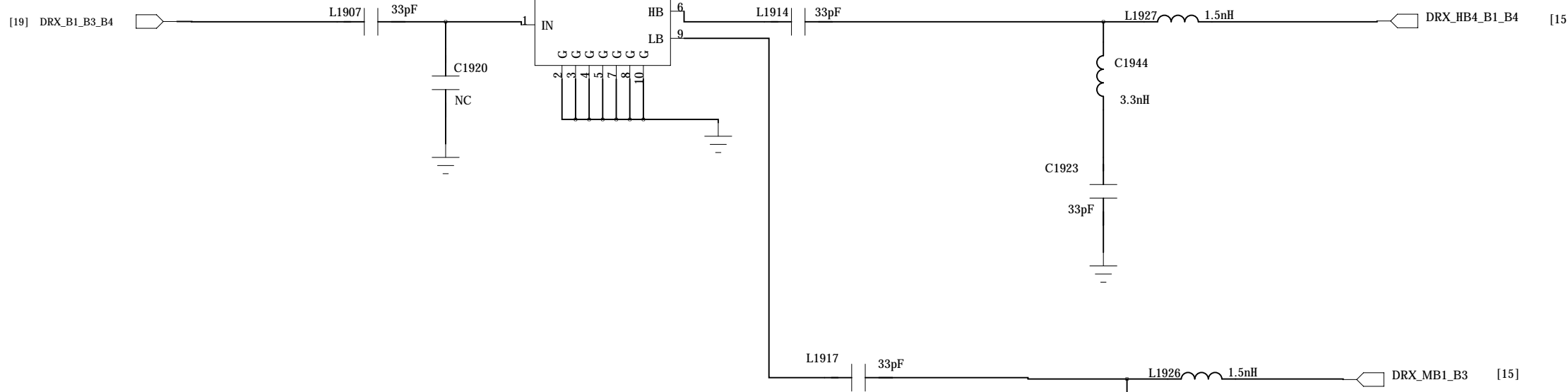
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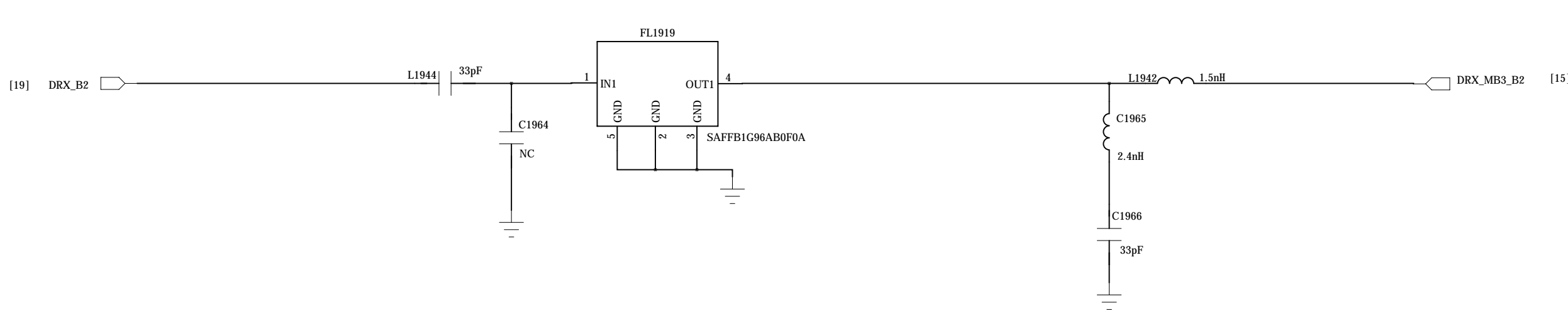
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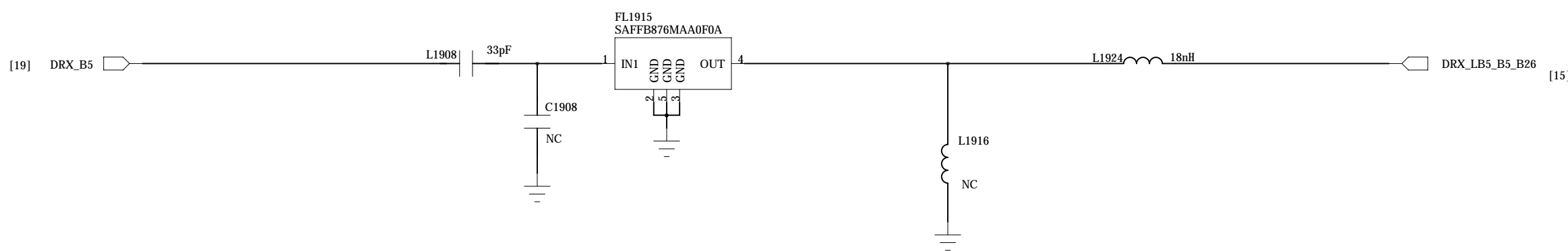
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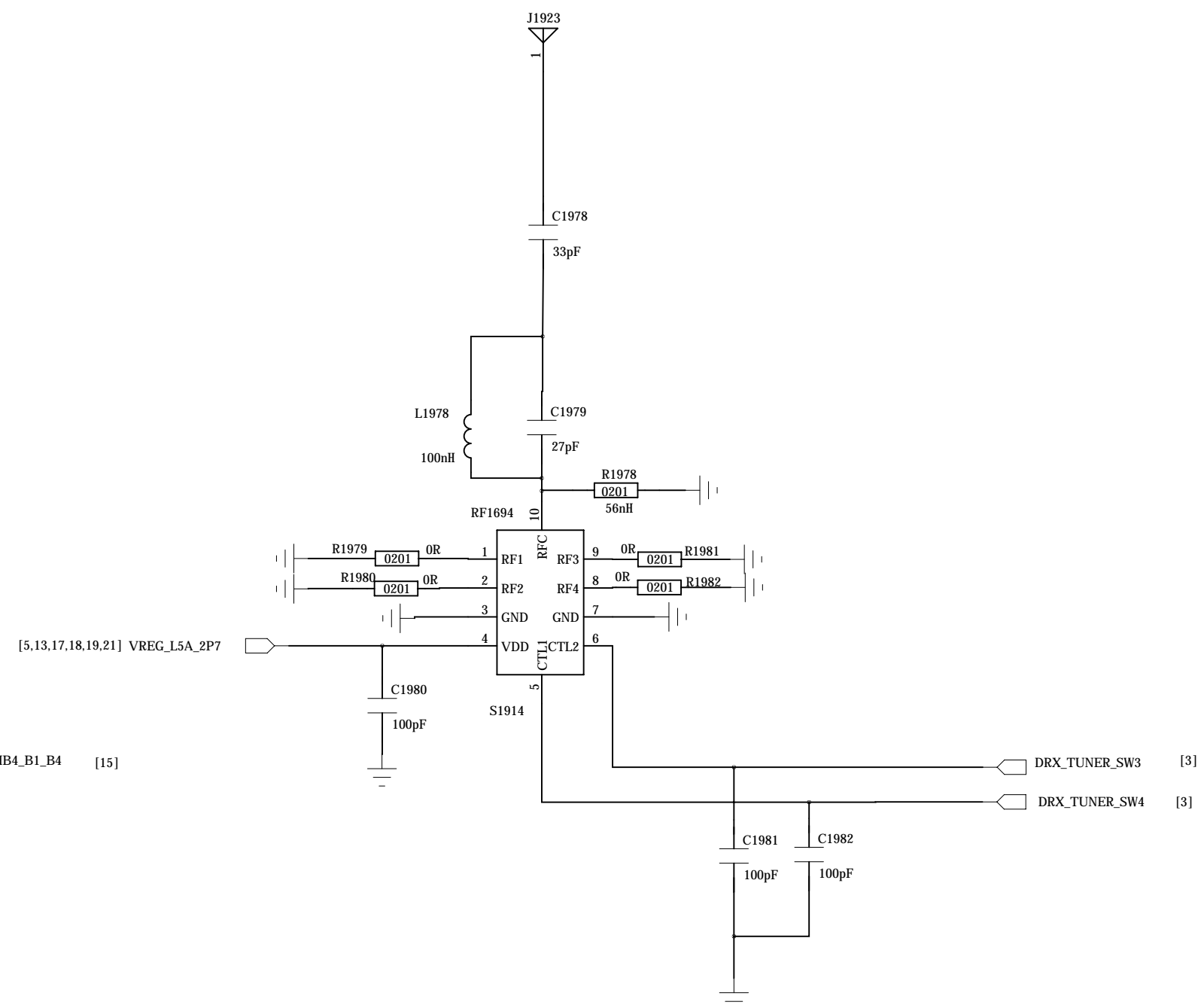
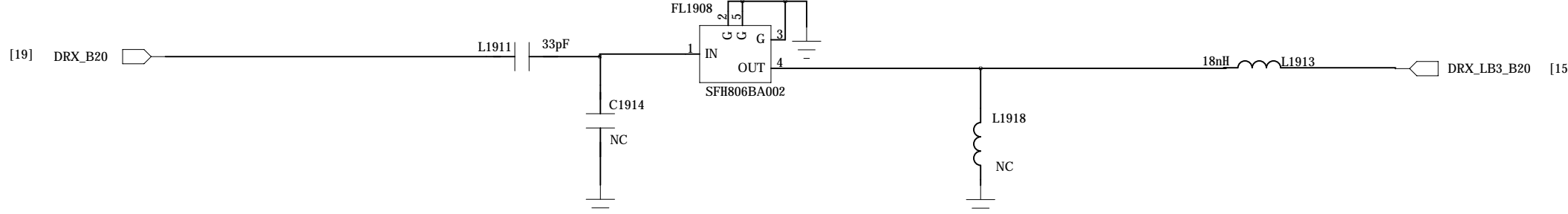


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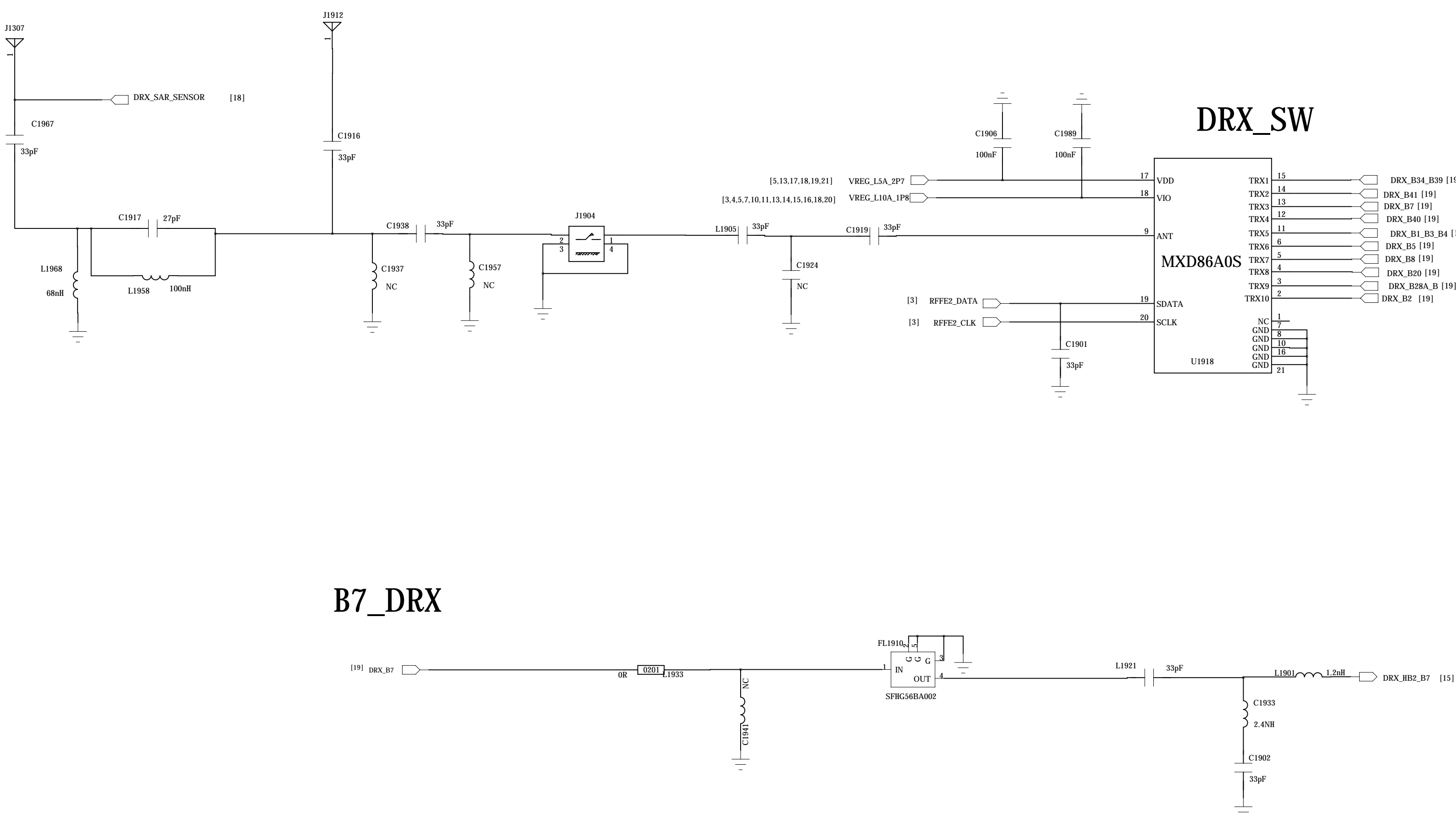


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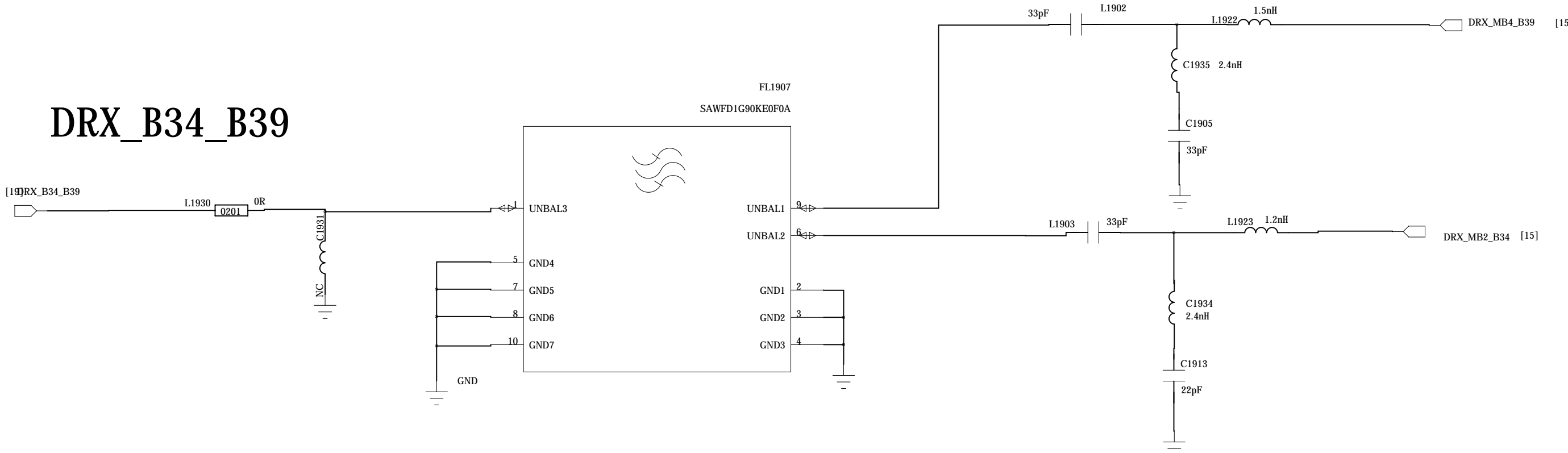
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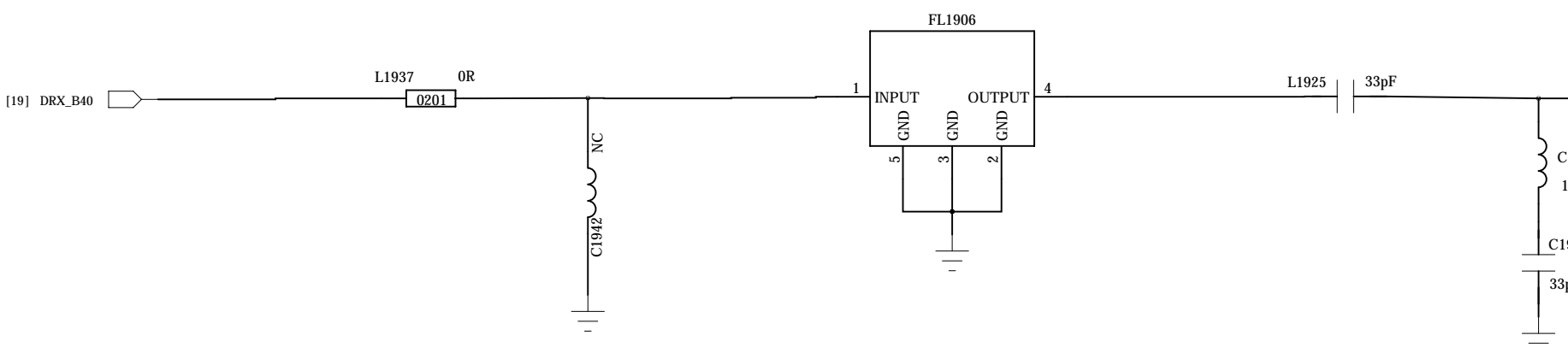
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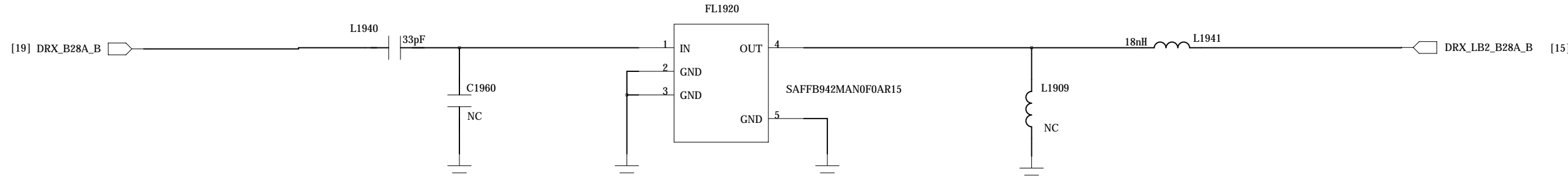
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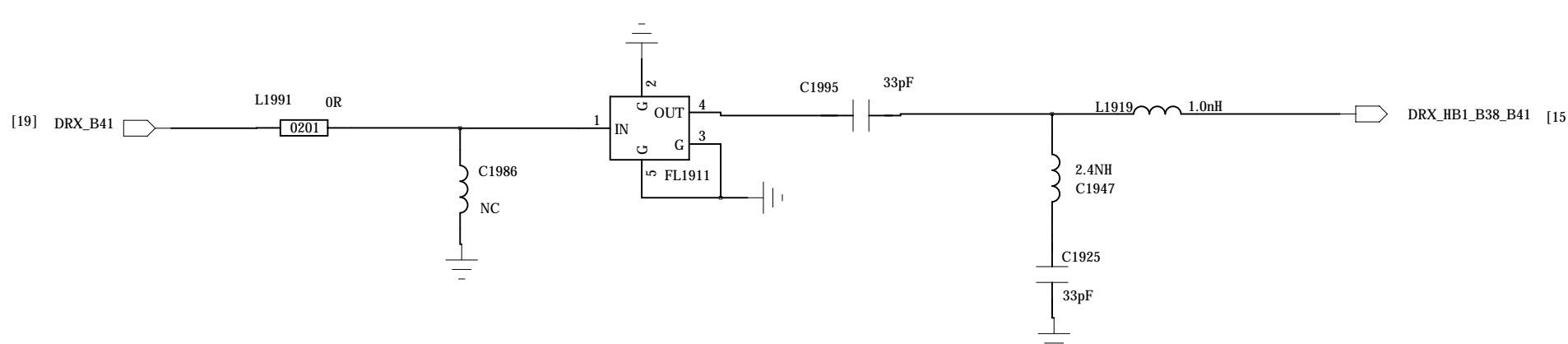
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B28 A+B DRX



DRX_B41_120M



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QUALITY CONTROL: <QC By>	DATED: <QC Date>				
RELEASED: <Release By>	DATED: <Release Date>				



REVISION RECORD			
LTR	ECO NO.	APPROVED	DATE

COMPANY: <Company Name>			
TITLE: <Title>			
DRAWN: <Drawn By>	DATED: <Drawn Date>	CYCLE	SIZE
CHECKED: <Checked By>	DATED: <Checked Date>	DRAWING NO.	
QUALITY CONTROL: <QC By>	DATED: <QC Date>	REV:	
RELEASED: <Released By>	DATED: <Release Date>	SCALE: <Scale>	
		SHEET	22